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DAKAR10F/FG

2011.09.02

21-OCT-2002		
DATE	CHANGE NO.	REV

DRAWER		EE	DATE	POWER	DATE	<div>INVENTEC</div> <div>TITLEMODEL,PROJECT,FUNCTION Everest Main Board</div>				
DESIGN										
CHECK										
RESPONSIBLE										
SIZE=				VER:		SIZE		CODE	DOC.NUMBER	REV
FILE NAME:						C	CS			X01
P/N	xxx			1310xxxxx-0-0		SHEET		1 of 68		

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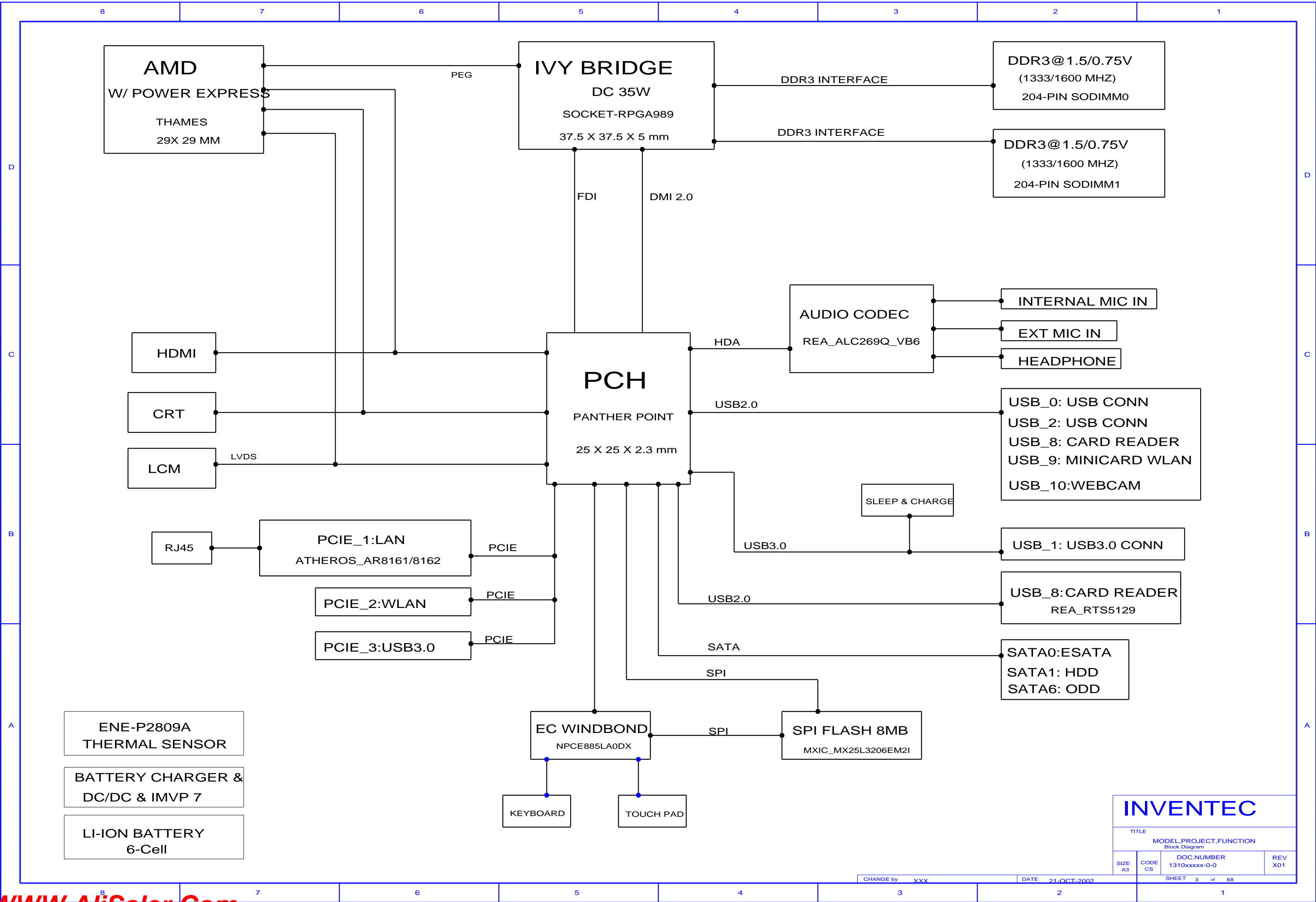
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TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

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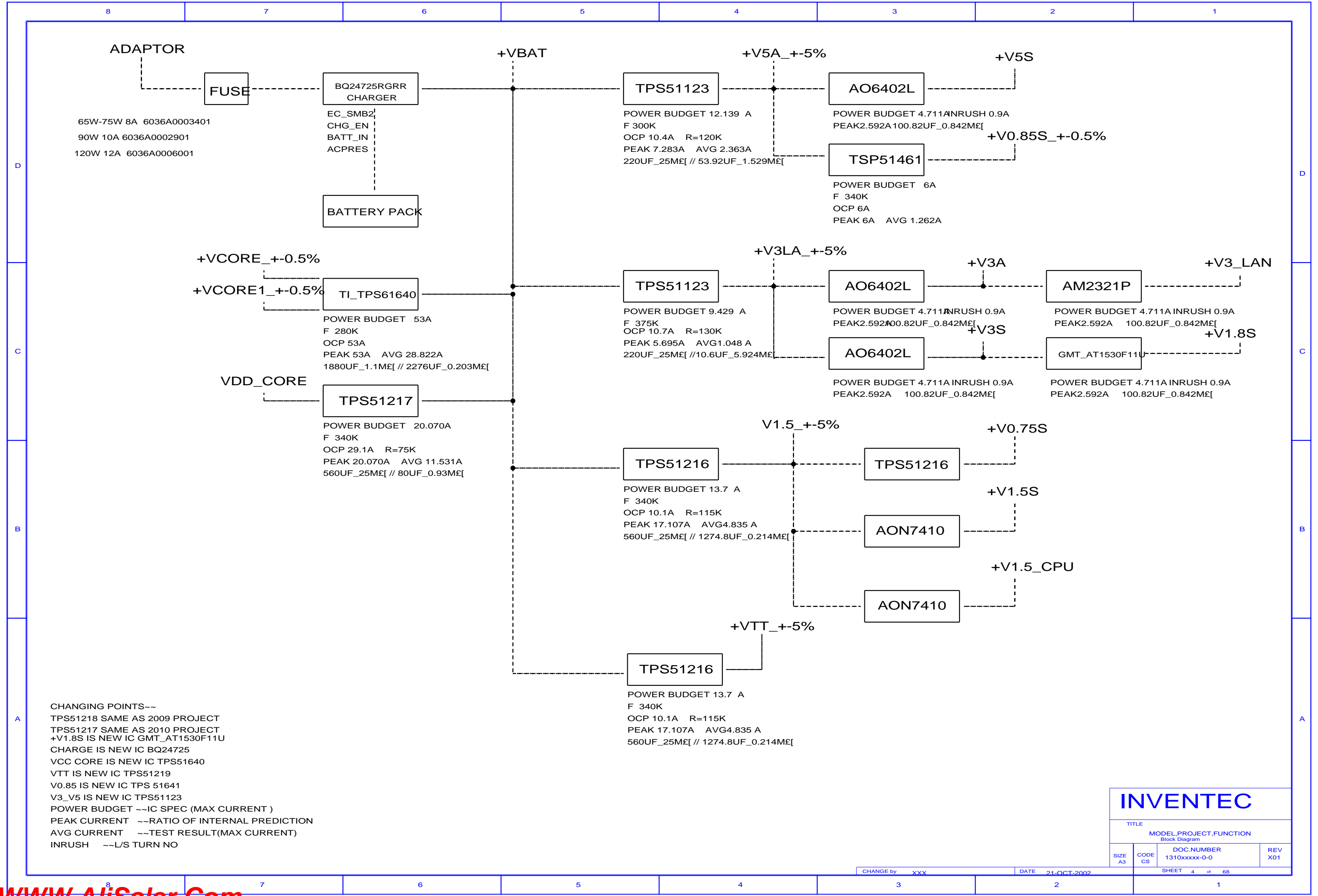
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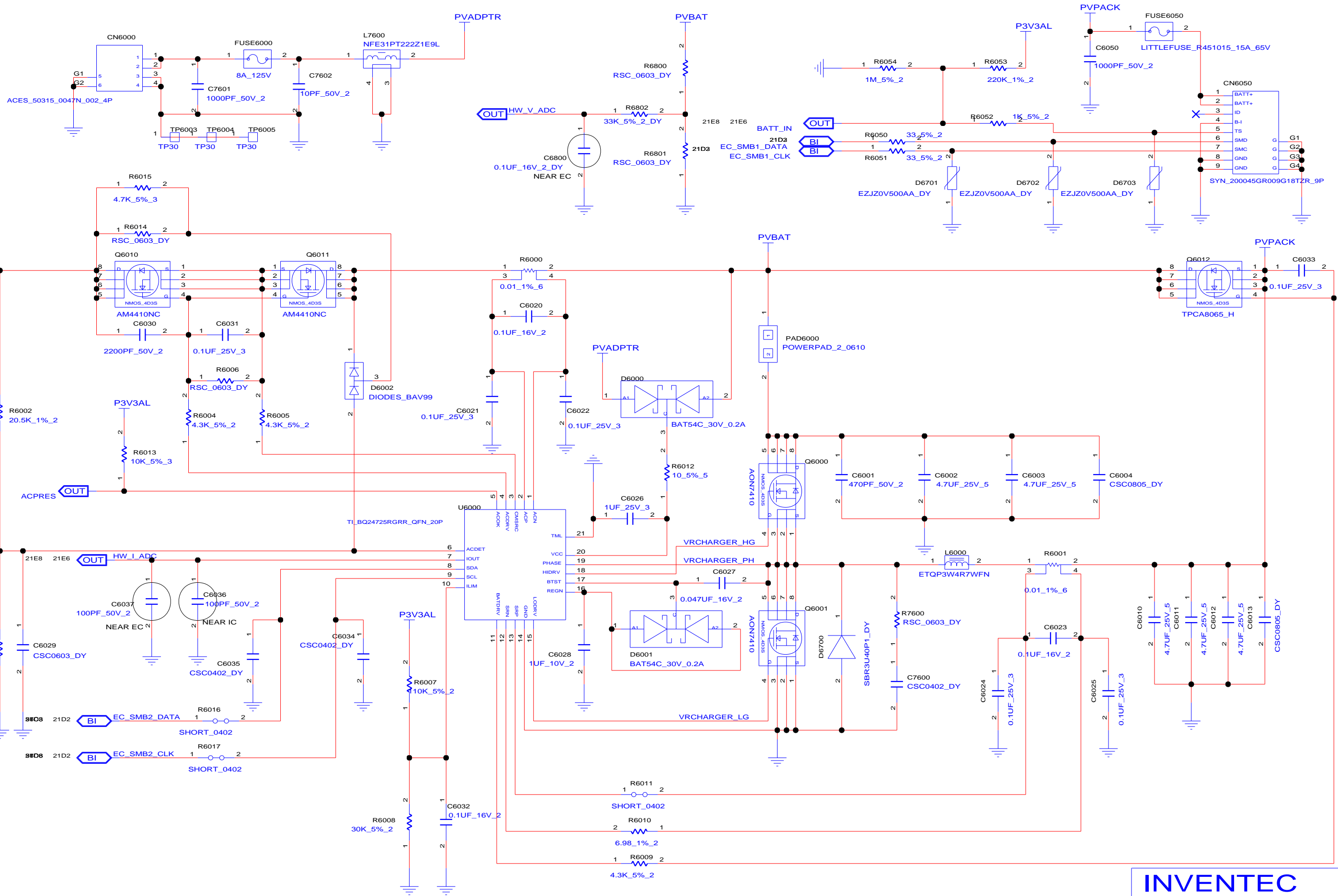
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MODEL,PROJECT,FUNCTION			
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CHANGING POINTS~~
TPS51218 SAME AS 2009 PROJECT
TPS51217 SAME AS 2010 PROJECT
+V1.8S IS NEW IC GMT_AT1530F11U
CHARGE IS NEW IC BQ24725
VCC CORE IS NEW IC TPS51640
VTT IS NEW IC TPS51219
V0.85 IS NEW IC TPS 51641
V3_V5 IS NEW IC TPS51123
POWER BUDGET ~~IC SPEC (MAX CURRENT)
PEAK CURRENT ~~RATIO OF INTERNAL PREDICTION
AVG CURRENT ~~TEST RESULT(MAX CURRENT)
INRUSH ~~L/S TURN NO

INVENTEC			
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FUSE6000
65W-75W 8A(6036A0003401)
90W 10A(6036A0002901)
120W 12A(6036A0006001)

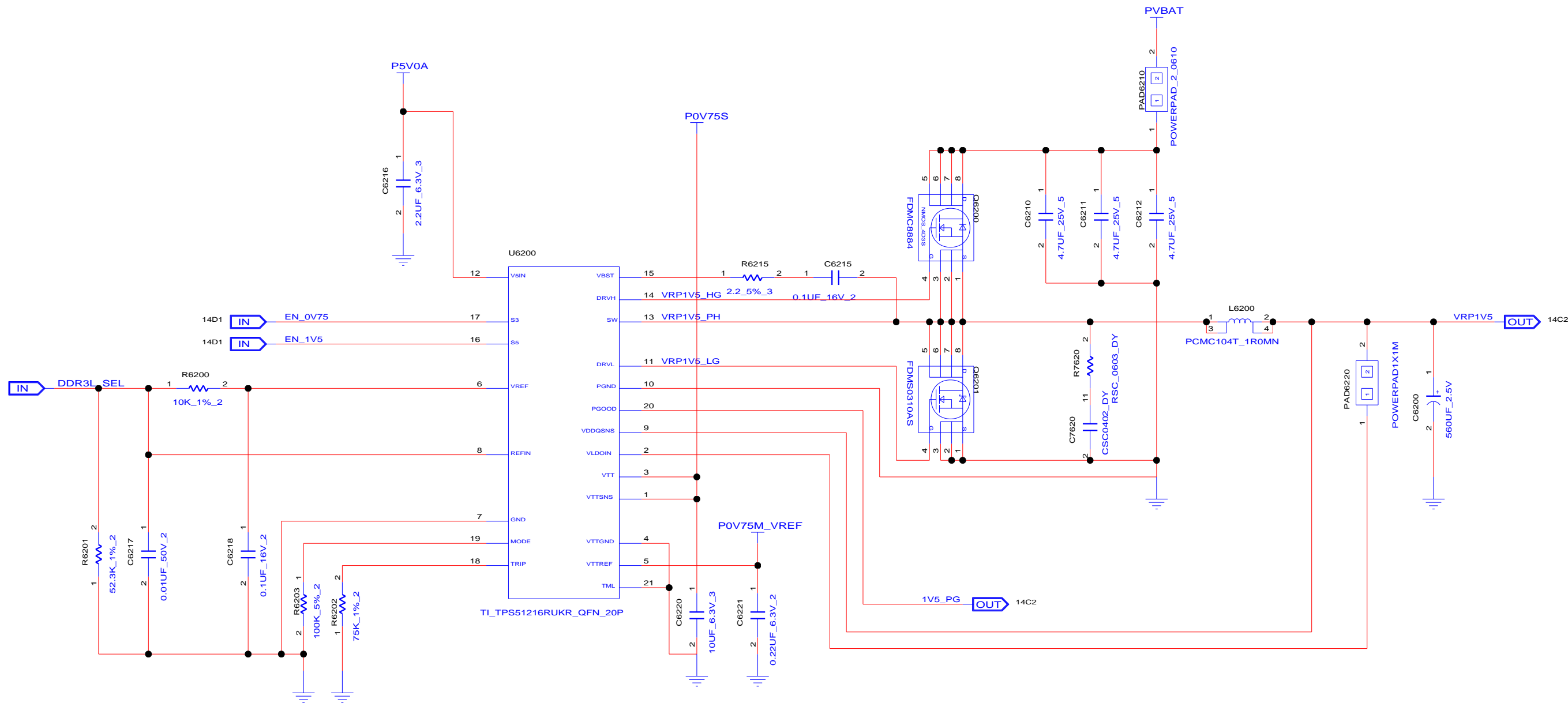


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$$VOUT=REFIN=1.8*(R6201/(R6200+R6201))$$
$$MODE=100KOHM:TRACKING DISCHARGE$$

STATE	S3	S5	VREF	VDDQ	VTTREF	VTT
S0	HI	HI	ON	ON	ON	ON
S3	LO	HI	ON	ON	ON	OFF(High-Z)
S4/S5	LO	LO	OFF	OFF(Discharge)	OFF(Discharge)	OFF(Discharge)

INVENTEC

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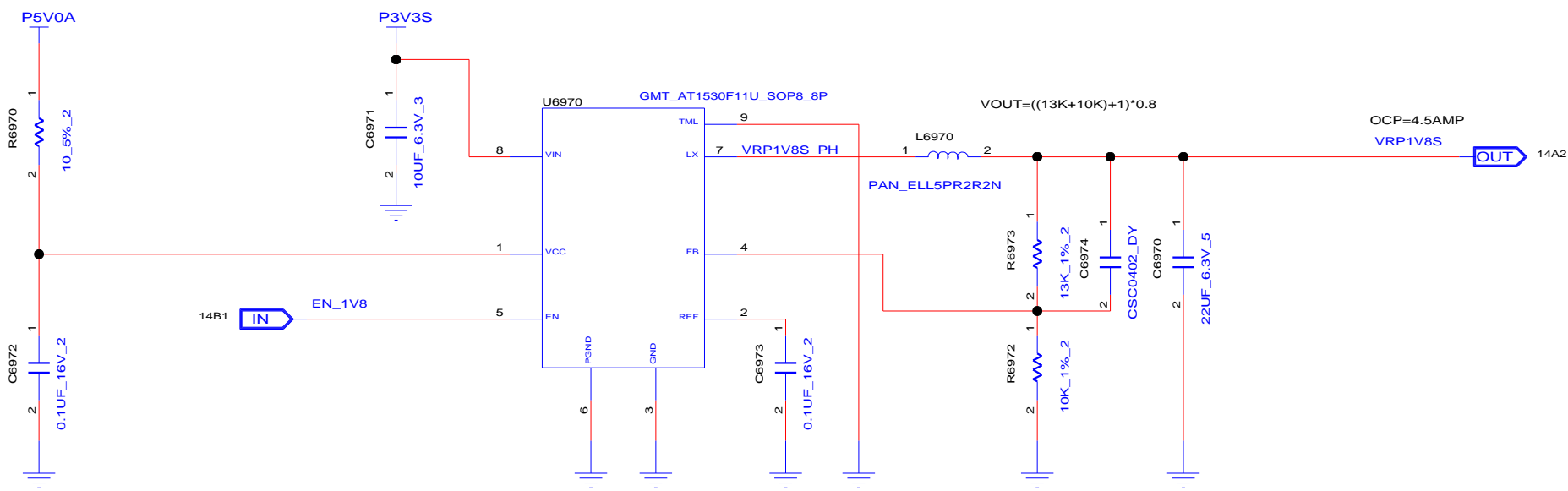
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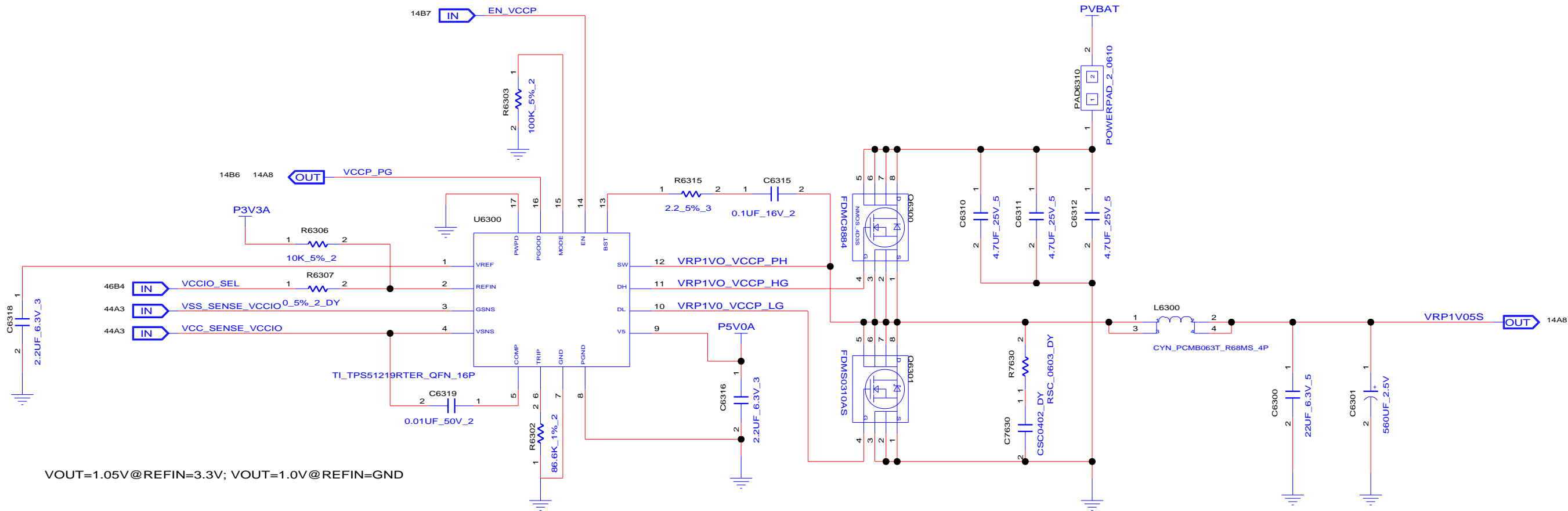
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VOUT=1.05V@REFIN=3.3V; VOUT=1.0V@REFIN=GND

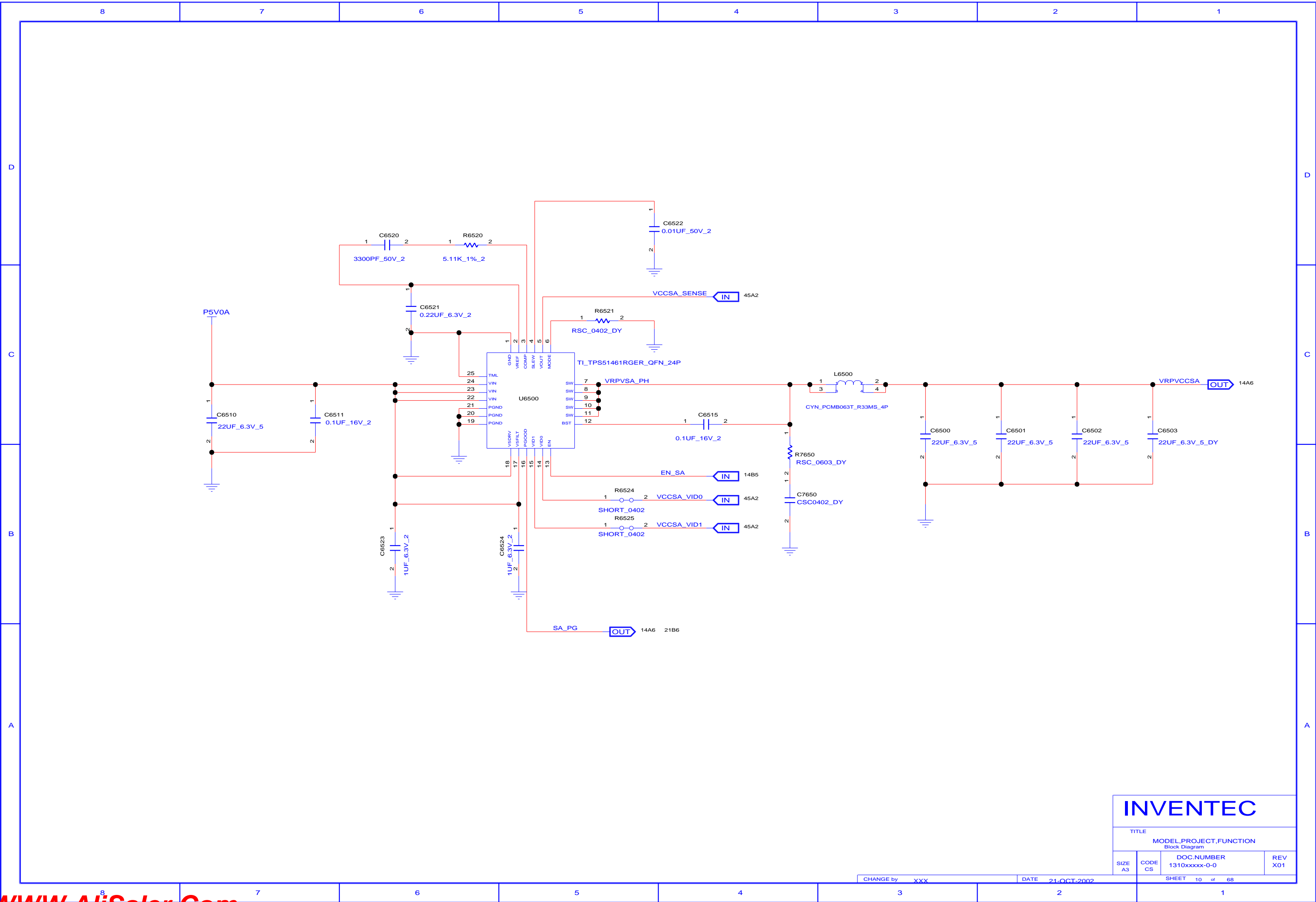
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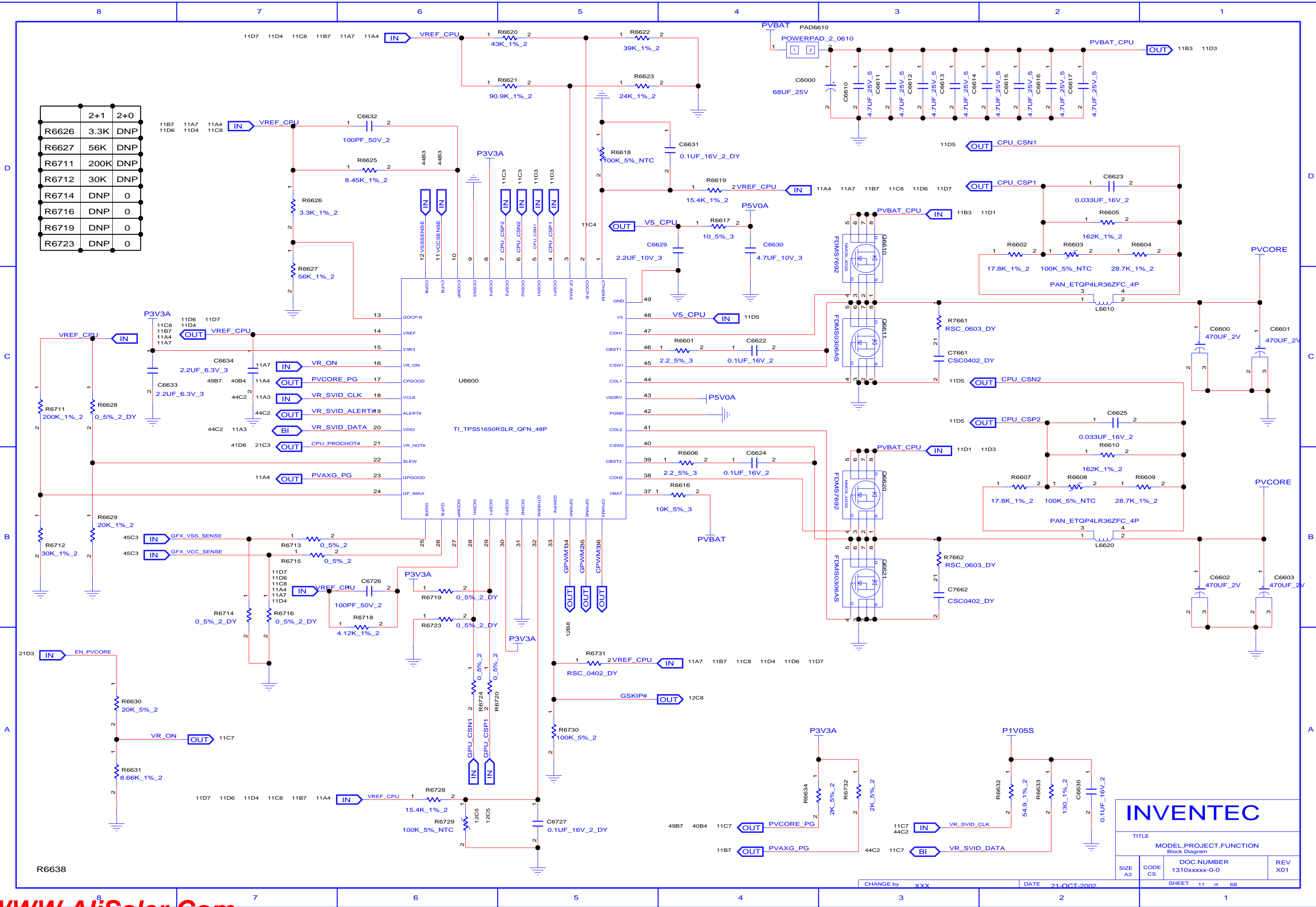
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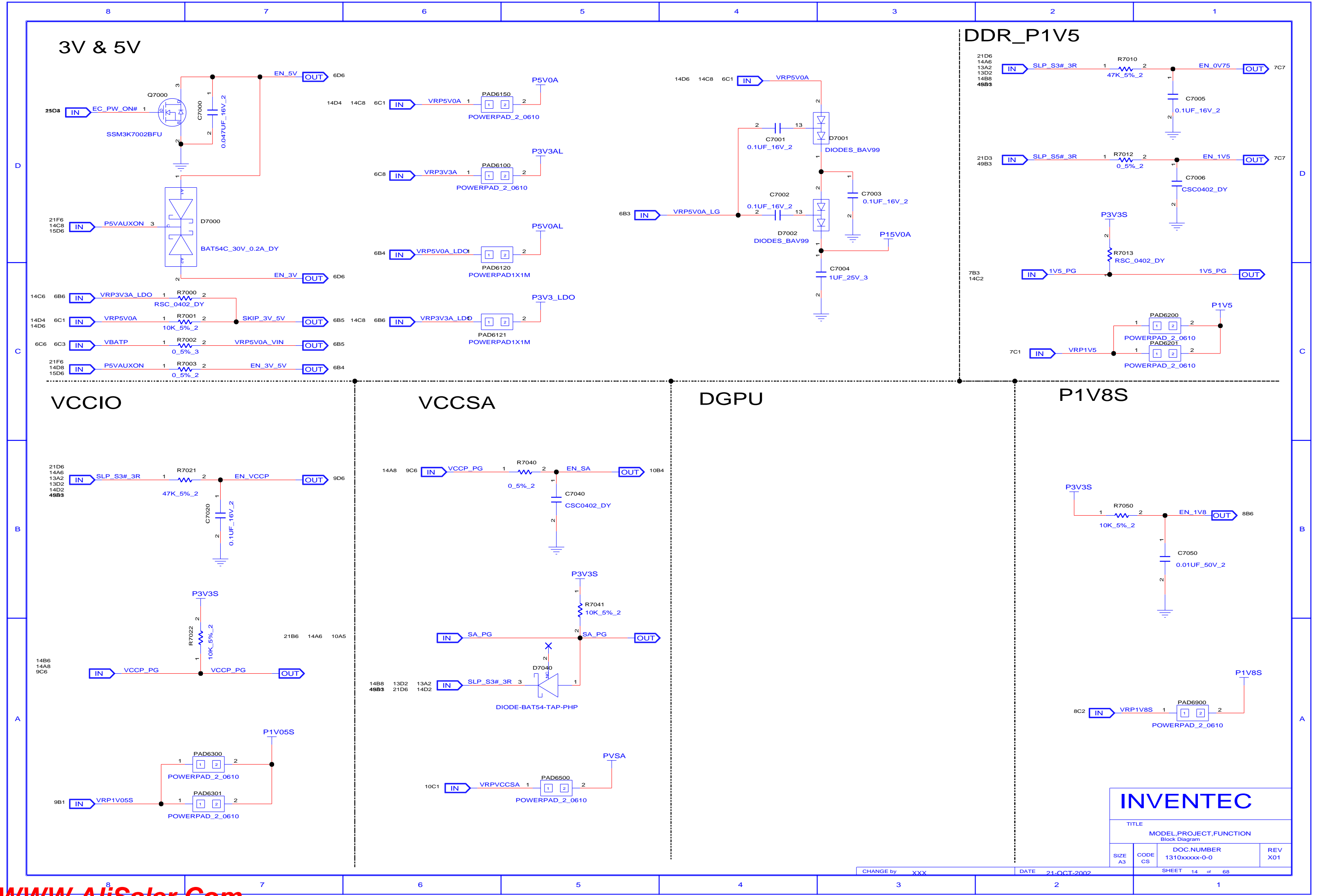
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	2+1	2+0
R6626	3.3K	DNP
R6627	56K	DNP
R6711	200K	DNP
R6712	30K	DNP
R6714	DNP	0
R6716	DNP	0
R6719	DNP	0
R6723	DNP	0



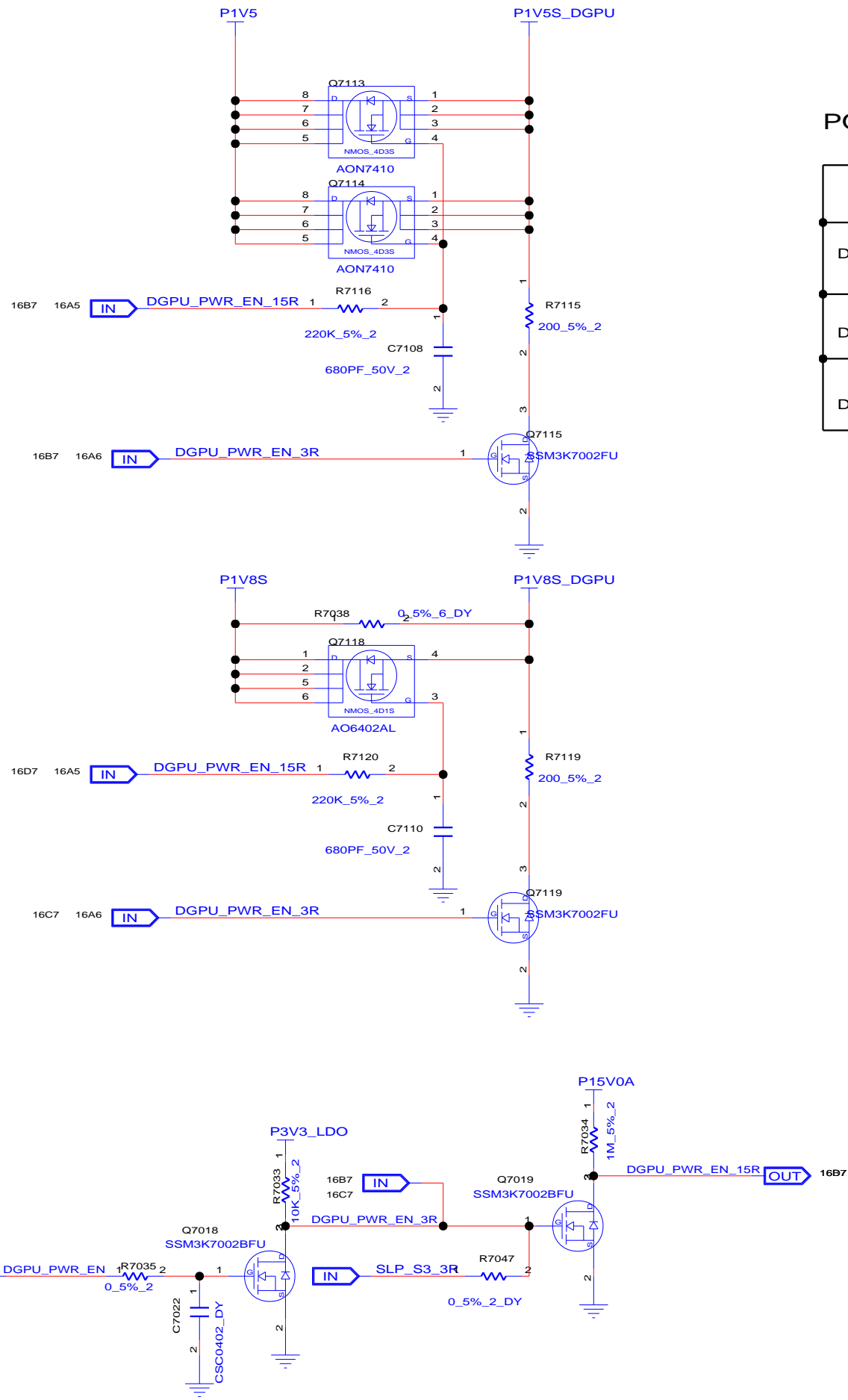


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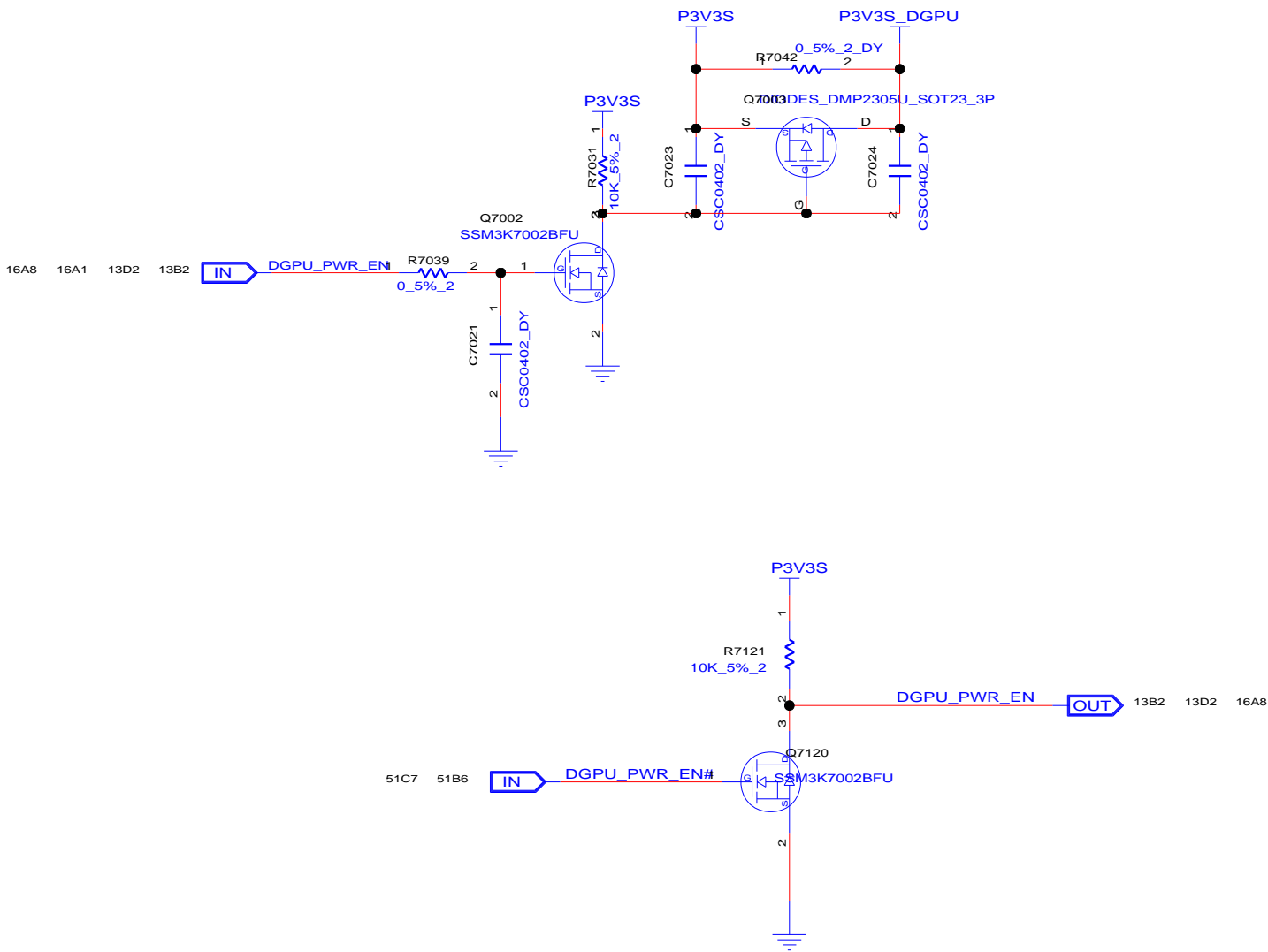
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POWER EXPRESS

	DURING RESET	AFTER RESET	
DGPU_PWR_EN#	HIGH	HIGH	0 : DGPU POWER SWITCH TURNED ON 1 : POWER SWITCH TURNED OFF
DGPU_PWRGD			0 : DGPU POWER IS NOT STABLE 1 : DGPU POWER IS STABLE
DGPU_HOLD_RST#	LOW	LOW	0 : KEEP DGPU IN RESET 1 : RESET IS RELEASED

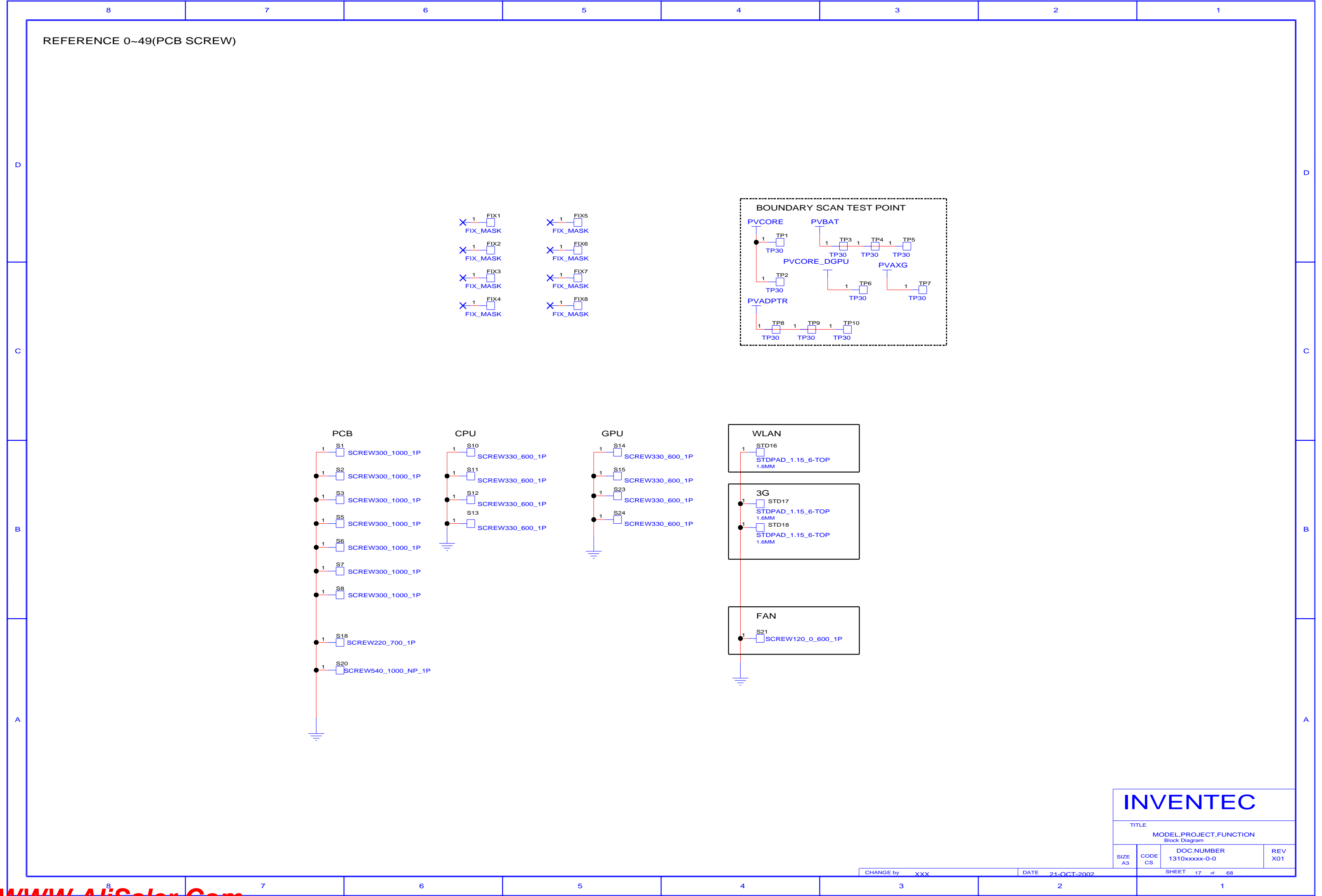


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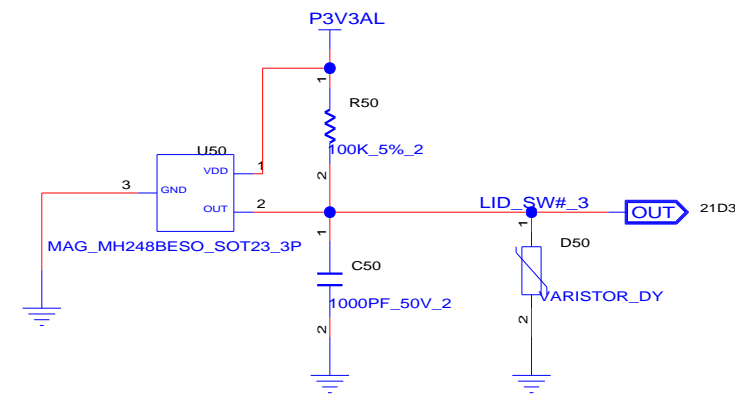
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REFERENCE 50-99(HALL SENSOR)

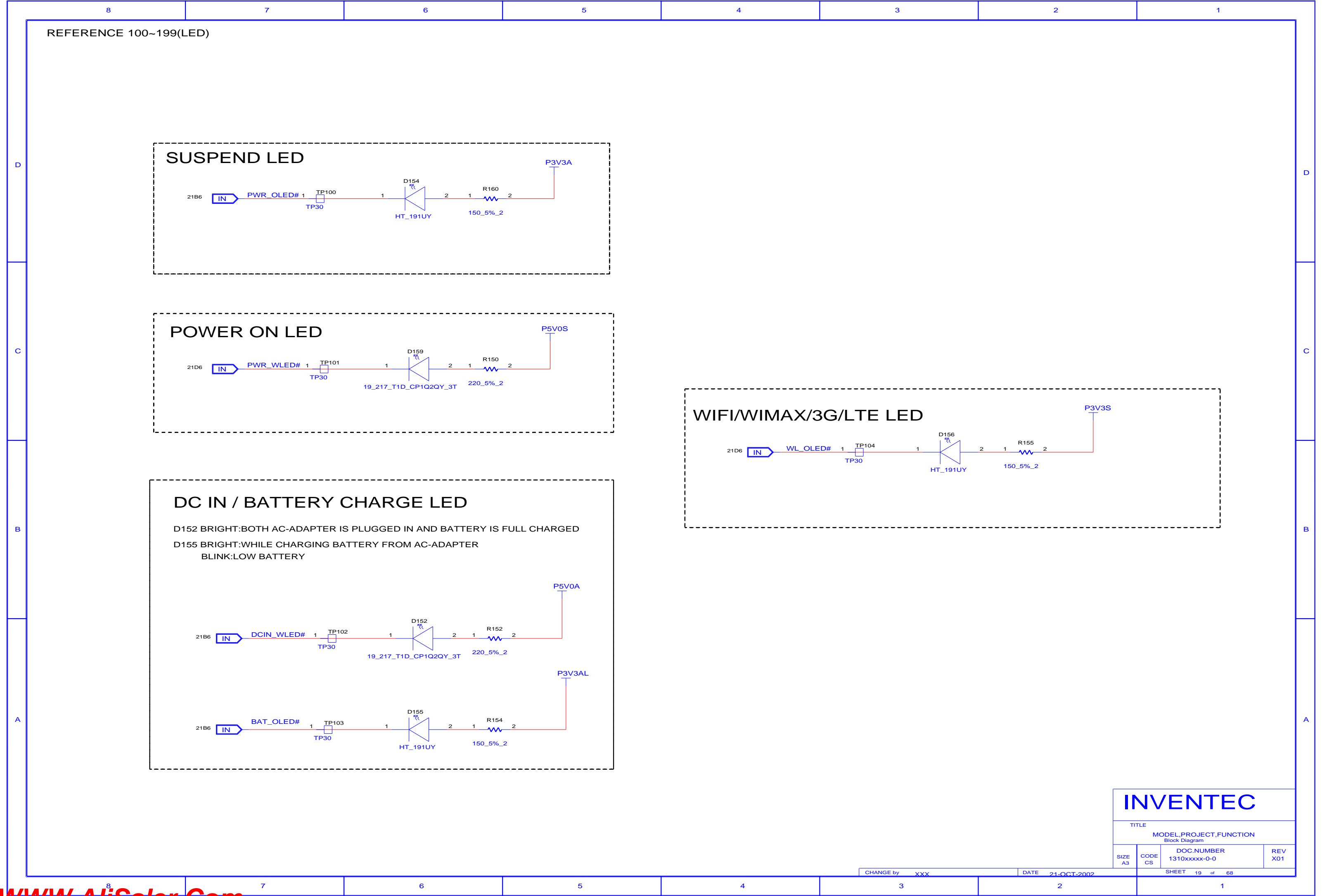


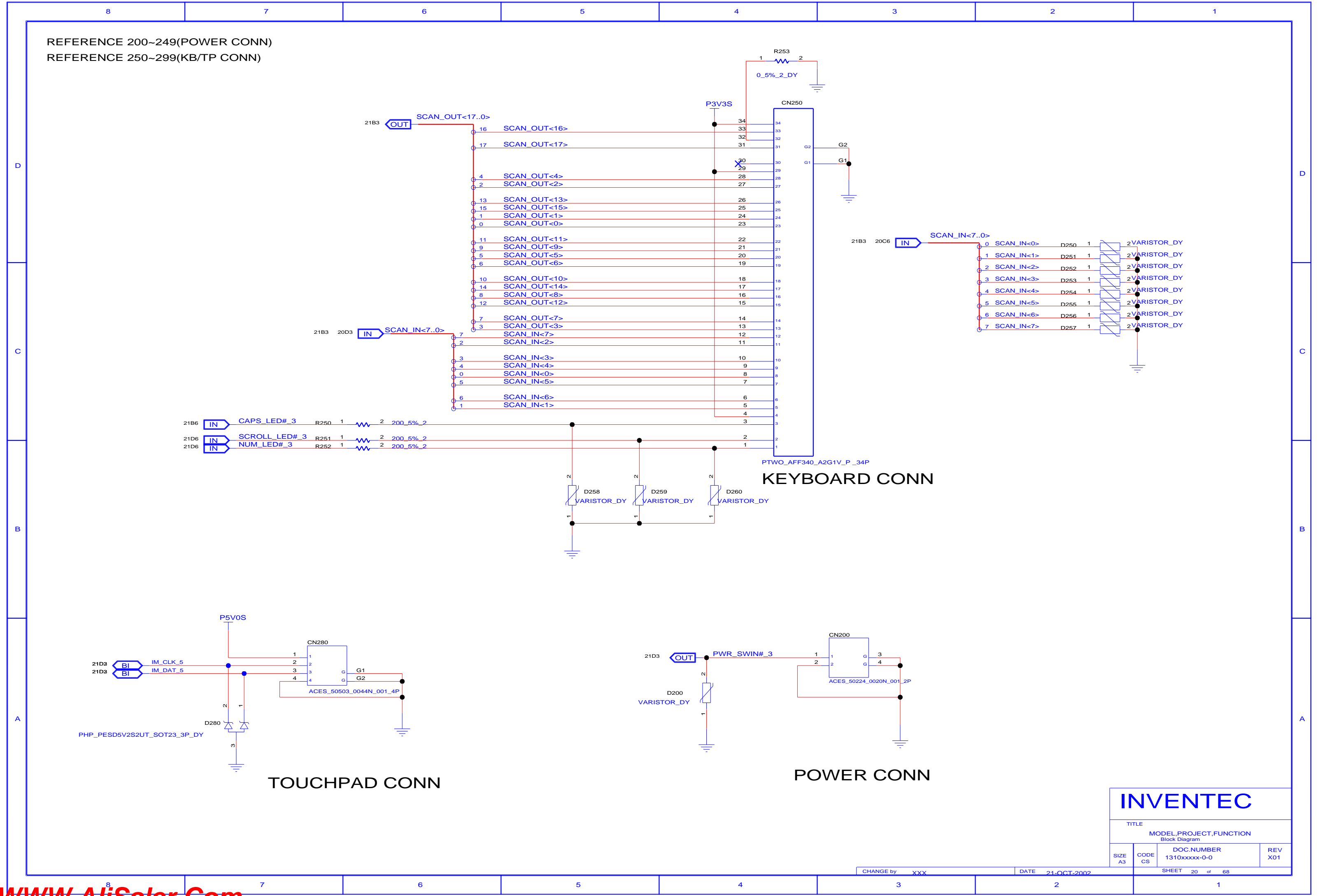
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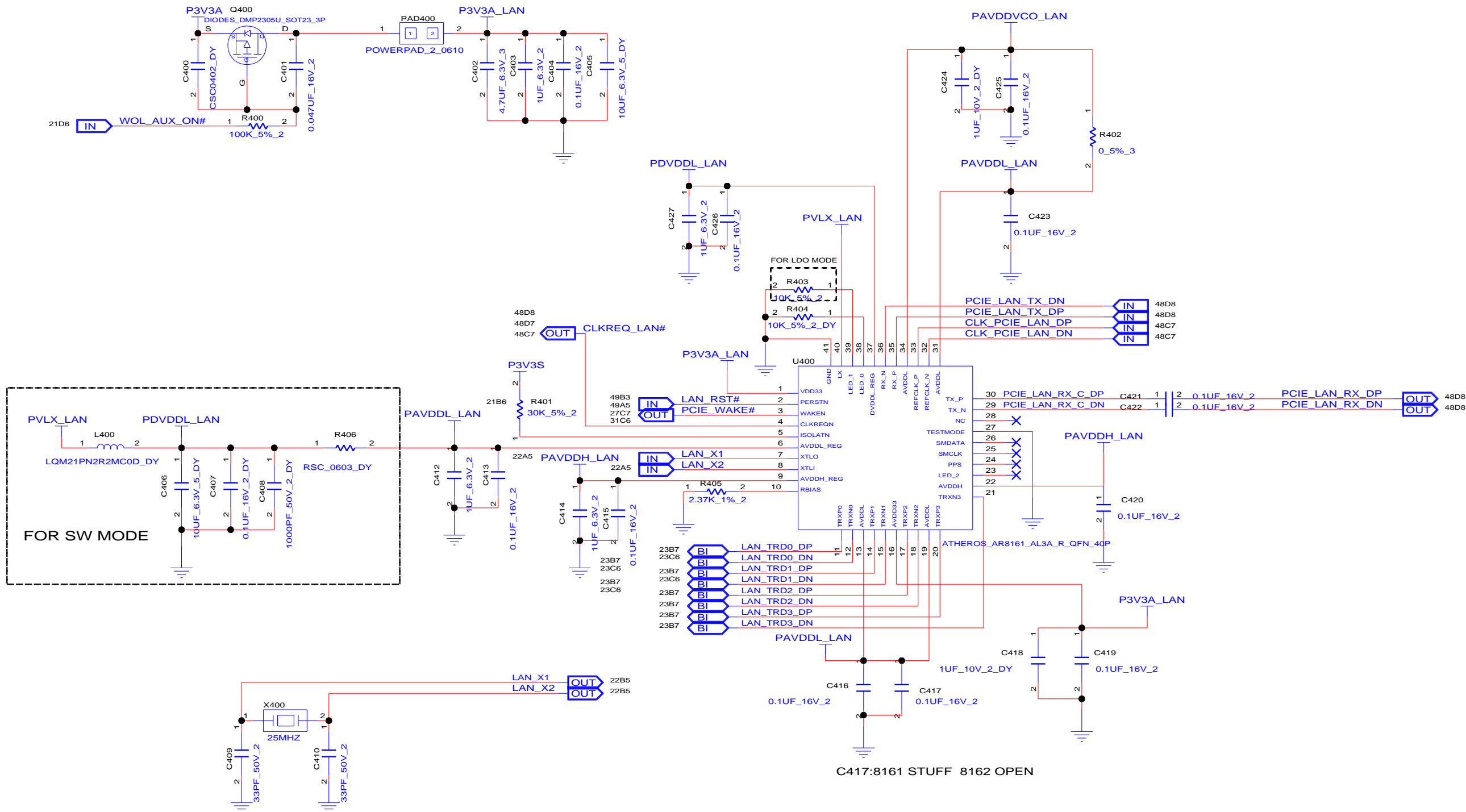
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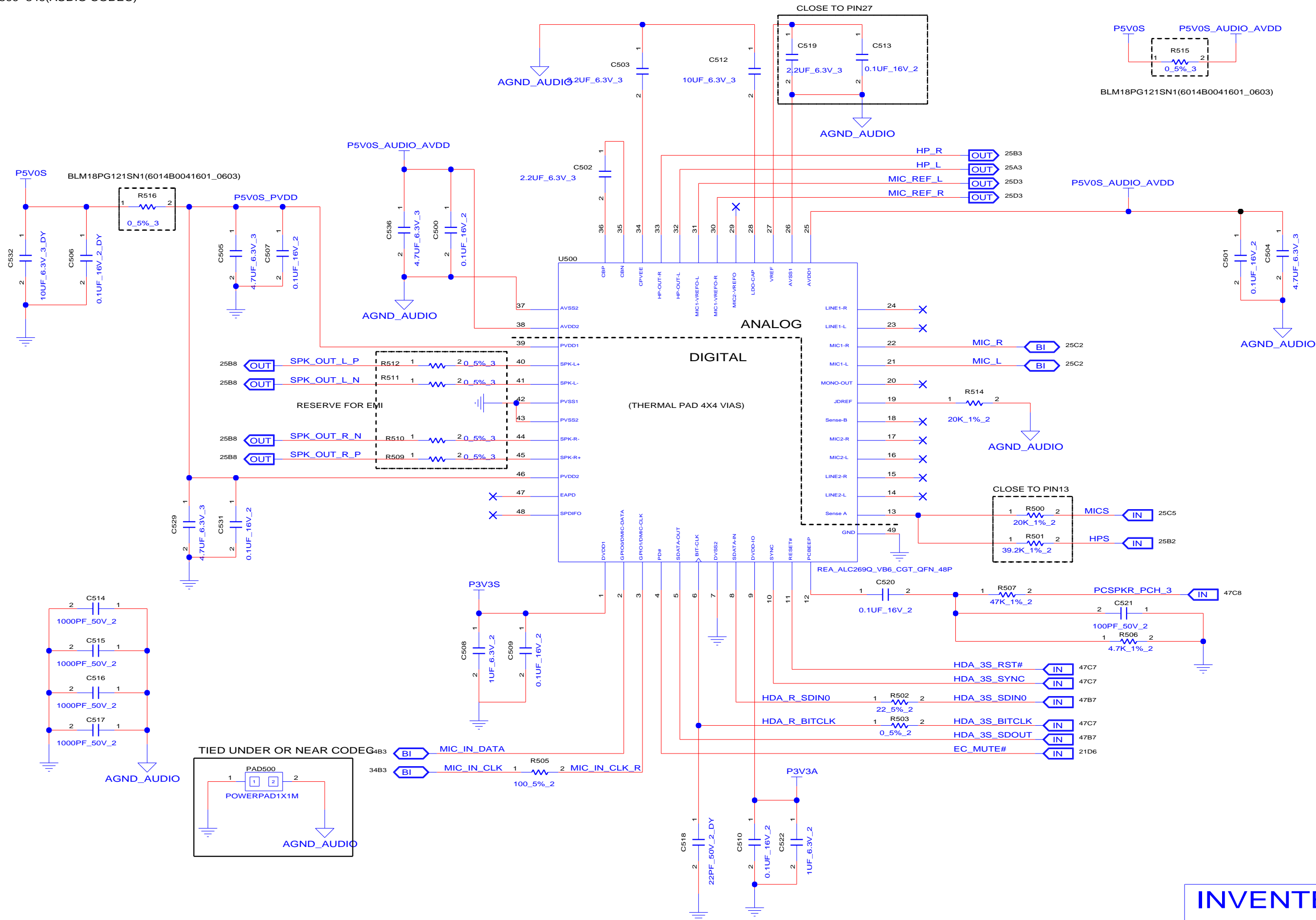
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REFERENCE 500~549(AUDIO CODEC)



INVENTEC

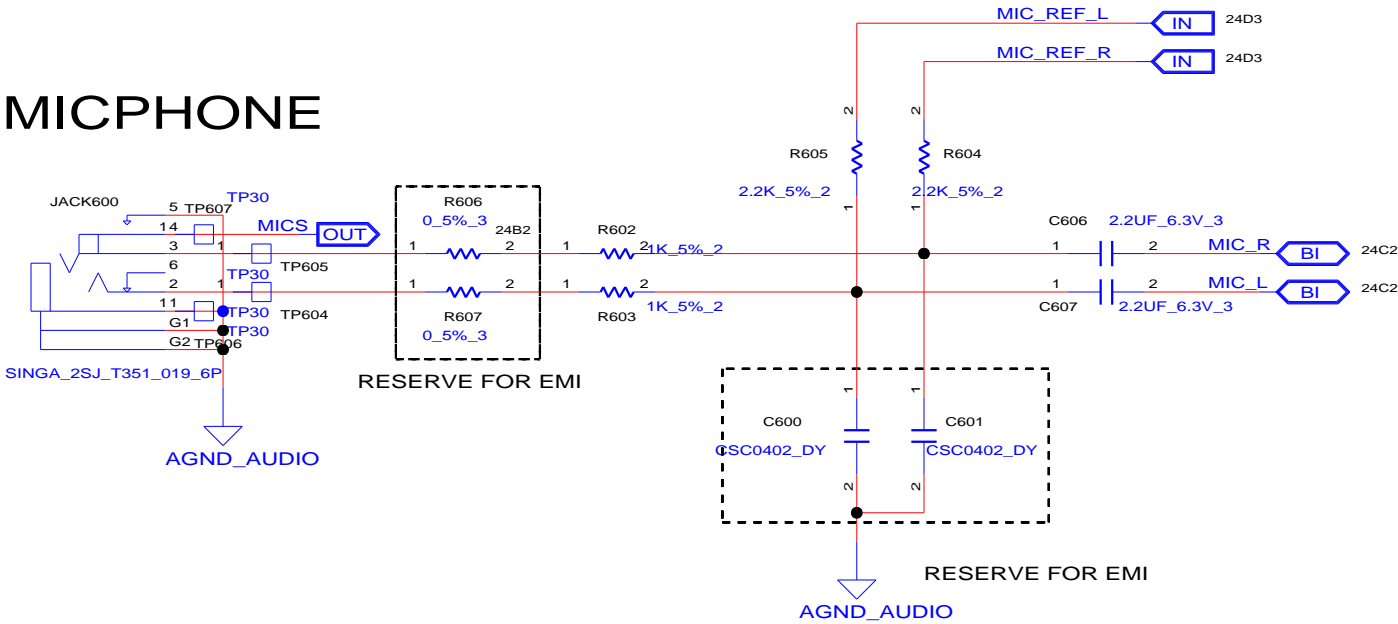
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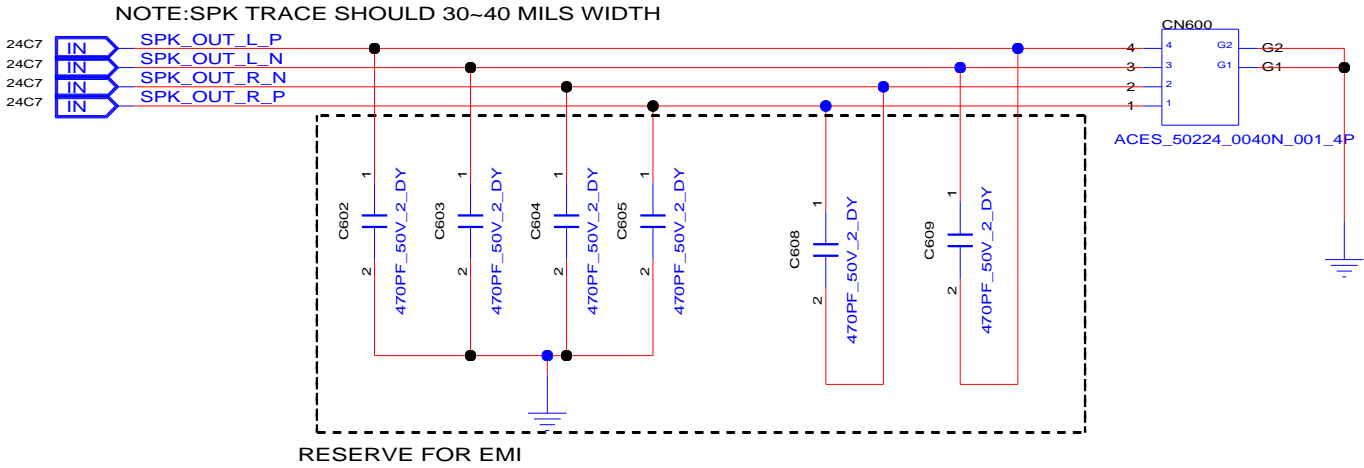
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AUDIO JACKS

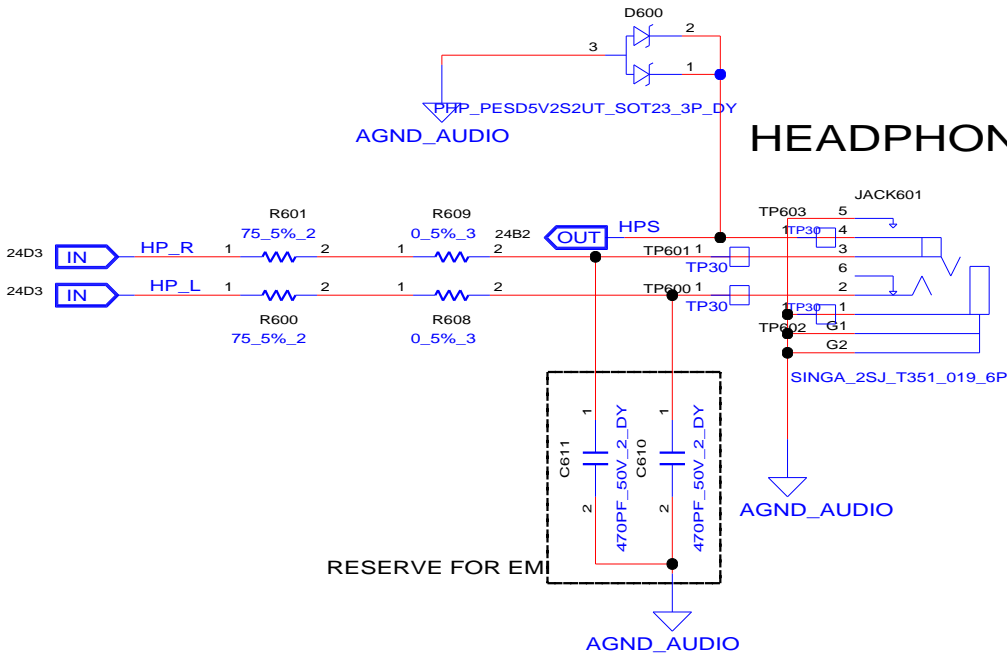
MICPHONE



INTERNAL SPEAKERS



HEADPHONE



INVENTEC

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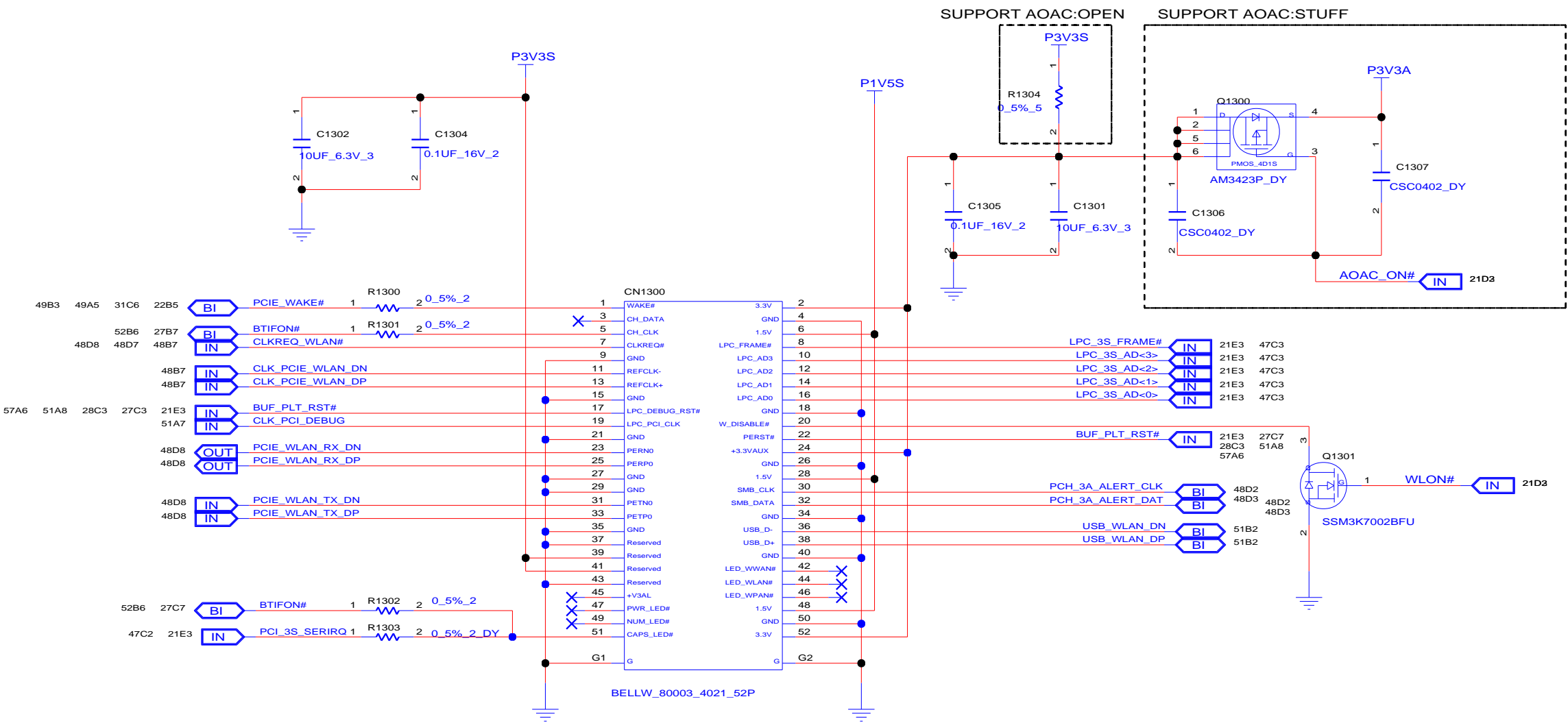
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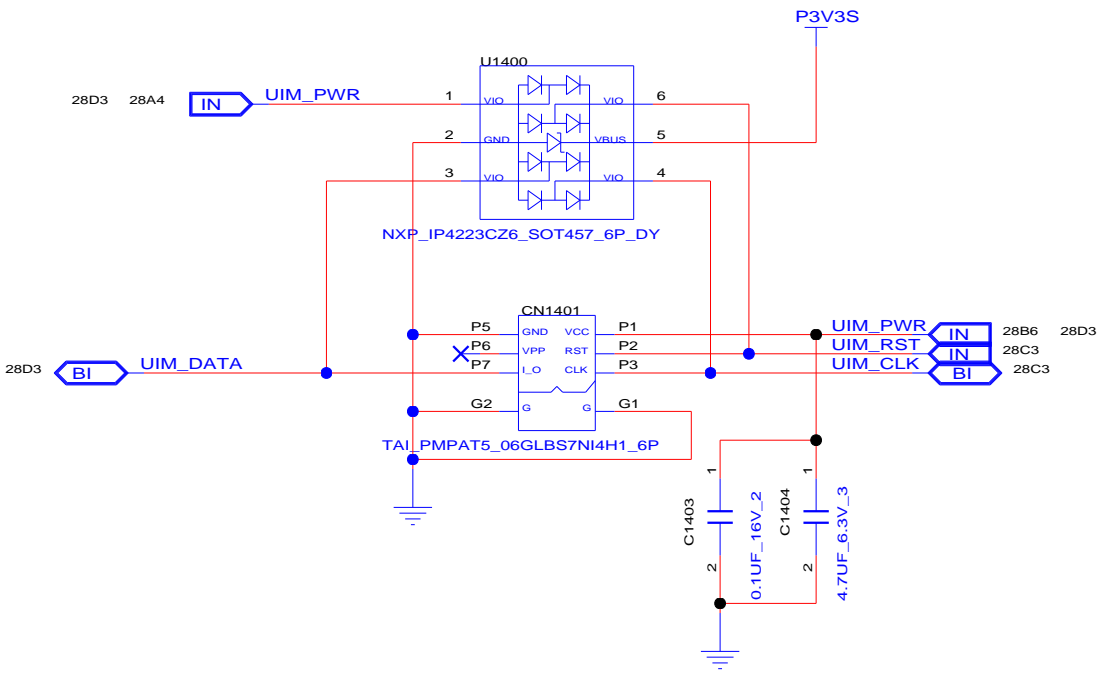
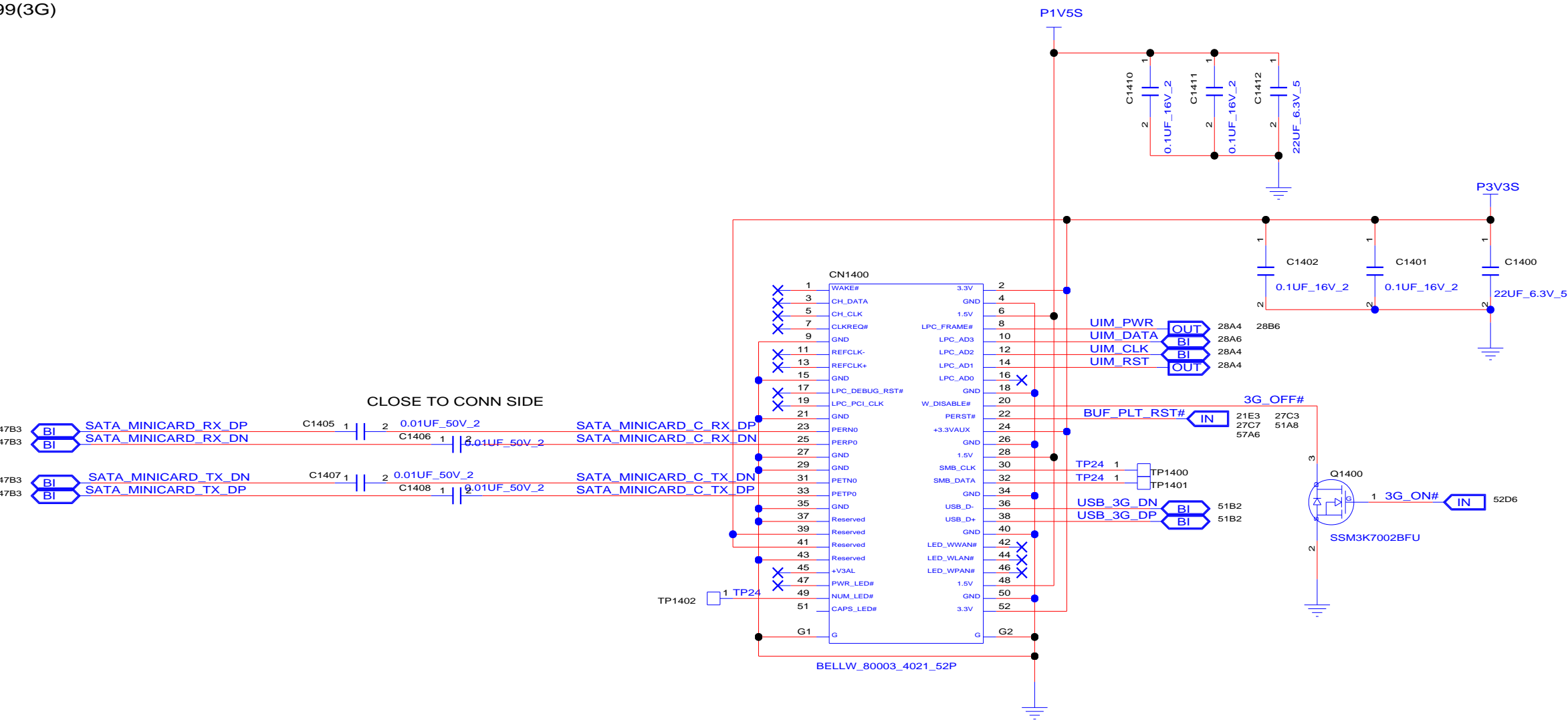
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MINI CARD 1(WLAN)

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REFERENCE 1400~1499(3G)



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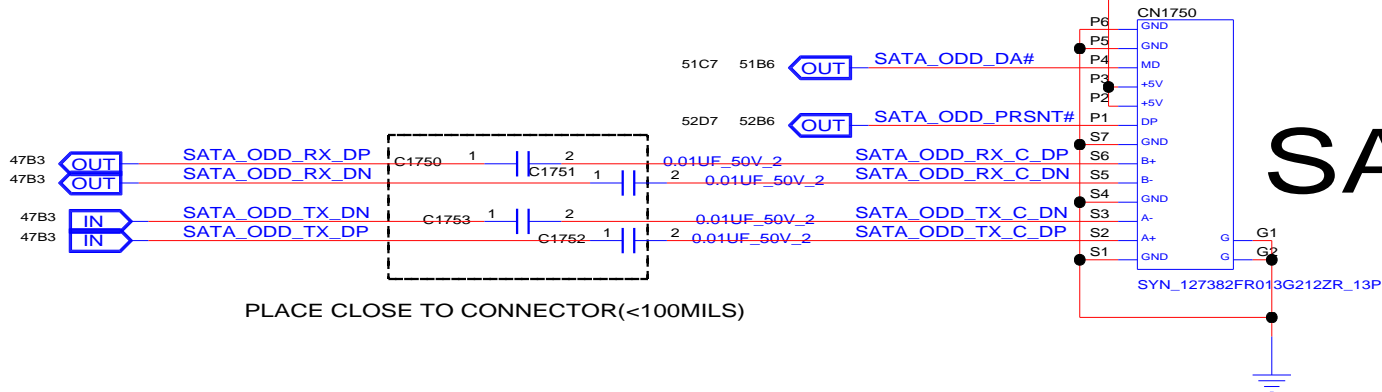
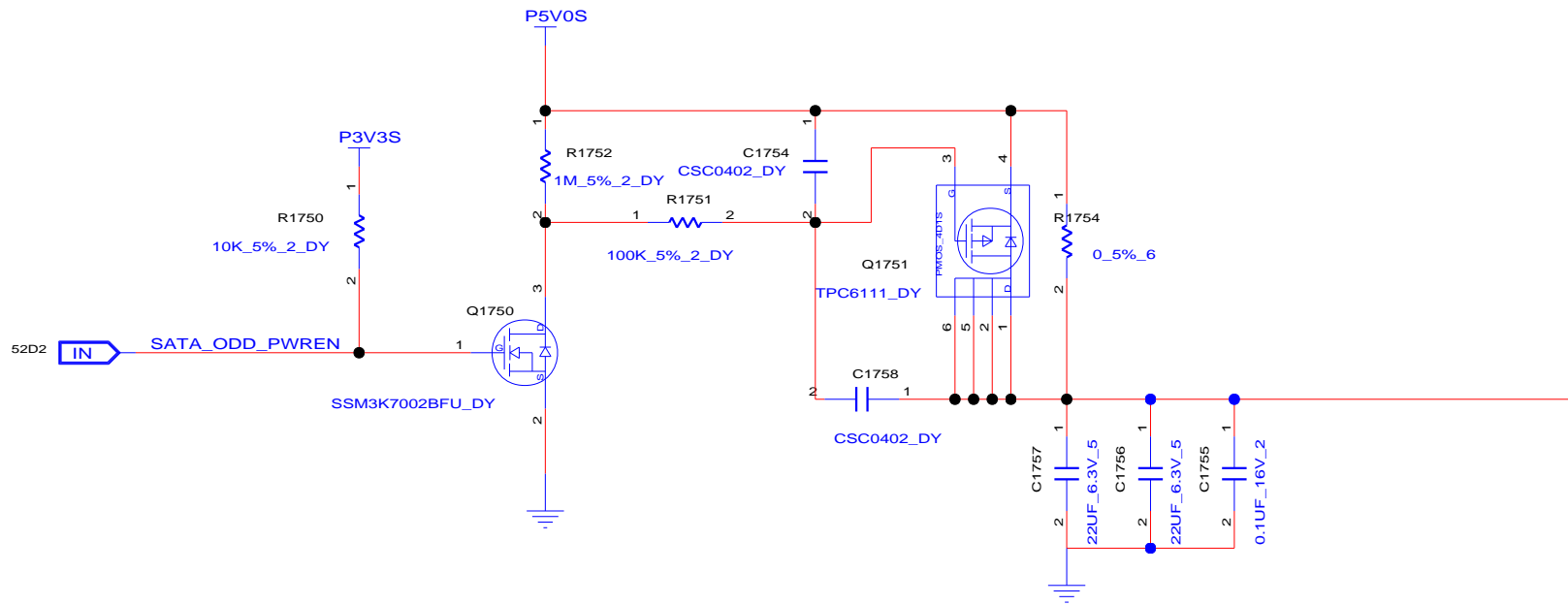
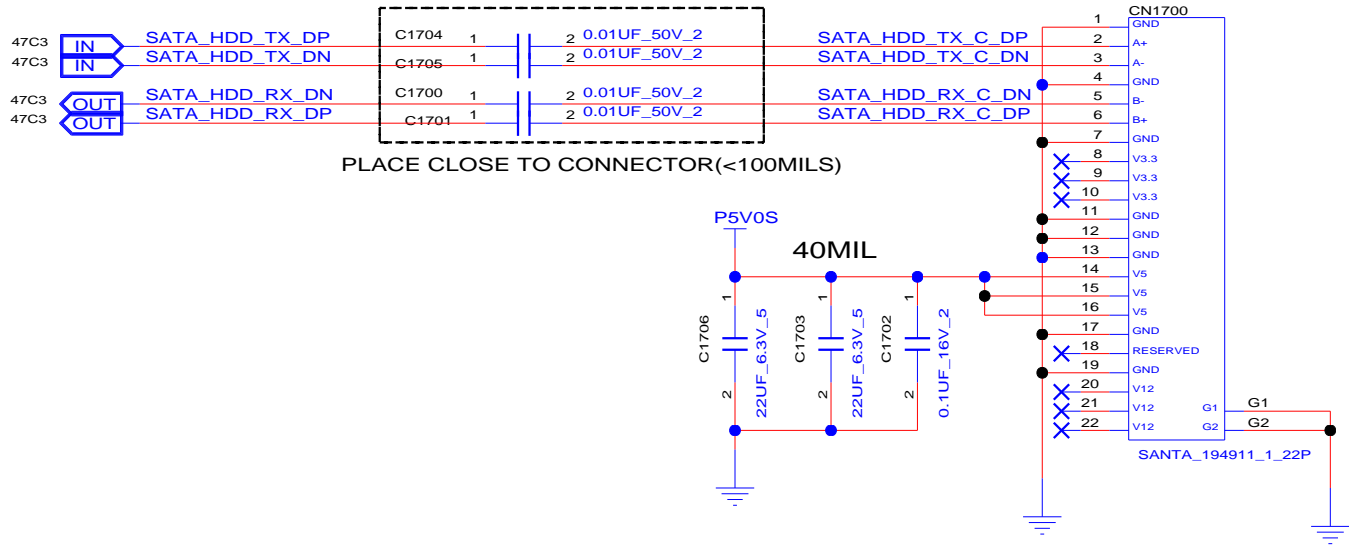
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REFERENCE 1700~1749(HDD)
REFERENCE 1750~1799(ODD)

SATA HDD



SATA ODD

INVENTEC

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SIZE A3 CODE CS

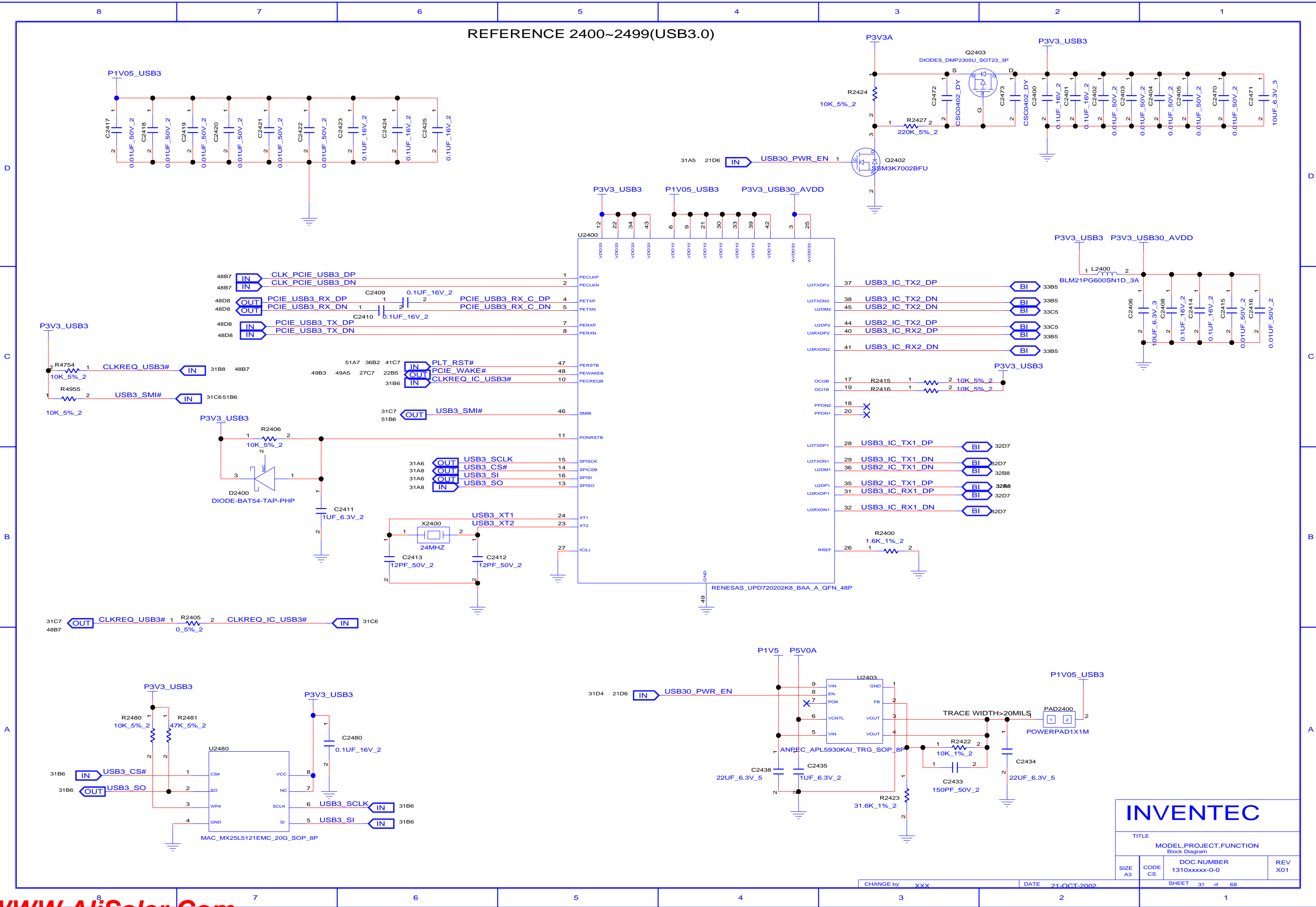
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REV X01

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REFERENCE 2400~2499(USB3.0)

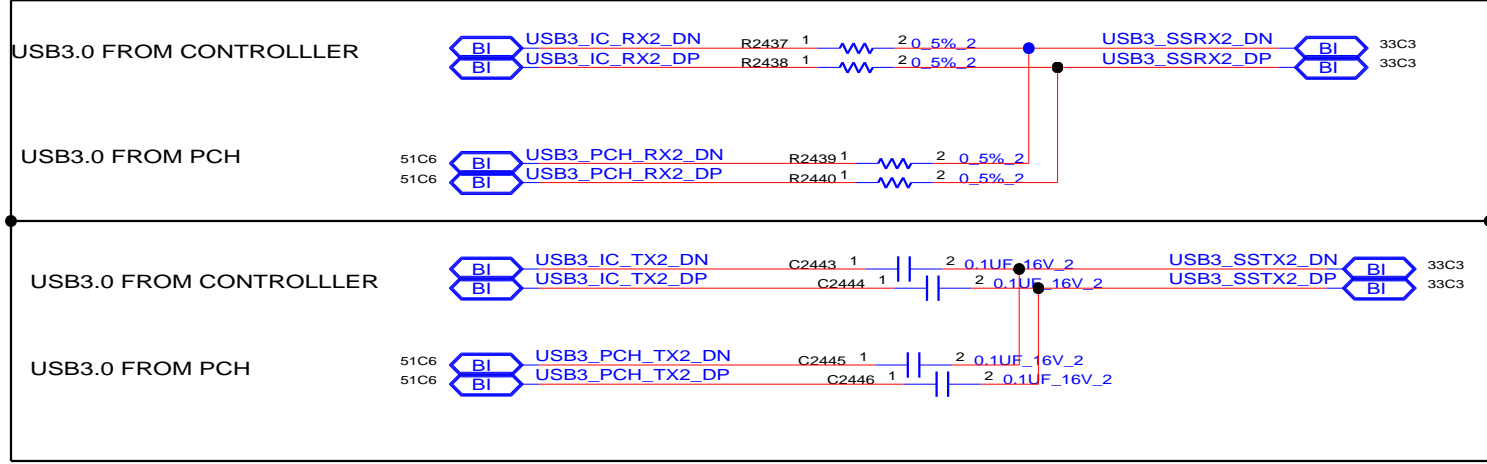
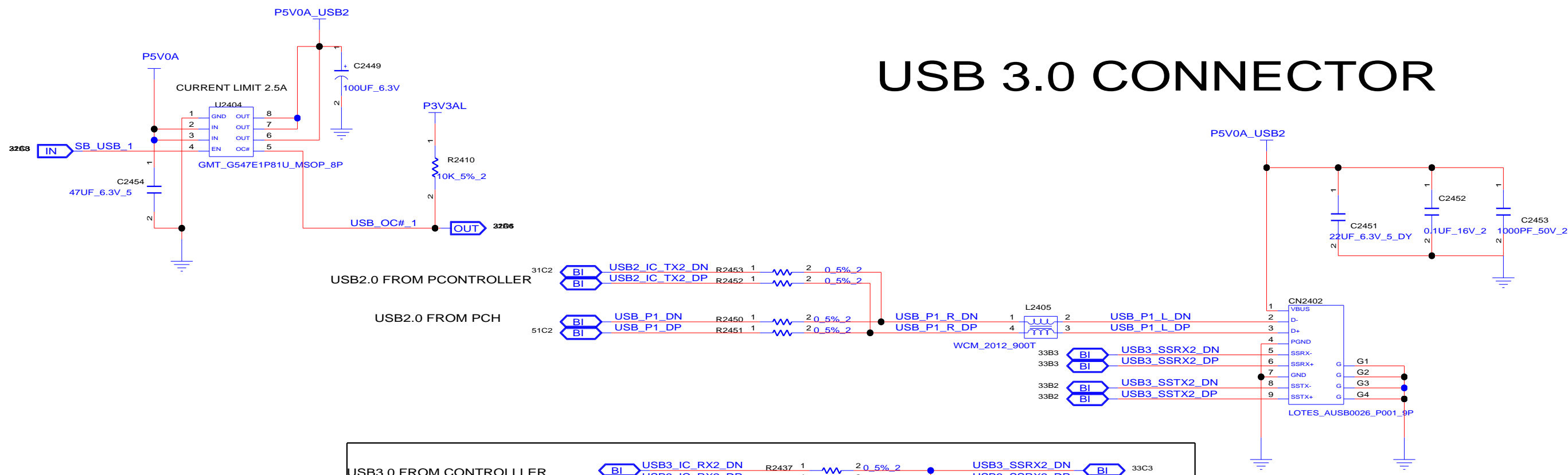


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REFERENCE 2400~2499(USB3.0)

USB 3.0 CONNECTOR



USB3.0

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REFERENCE 3000~3049(LCM)

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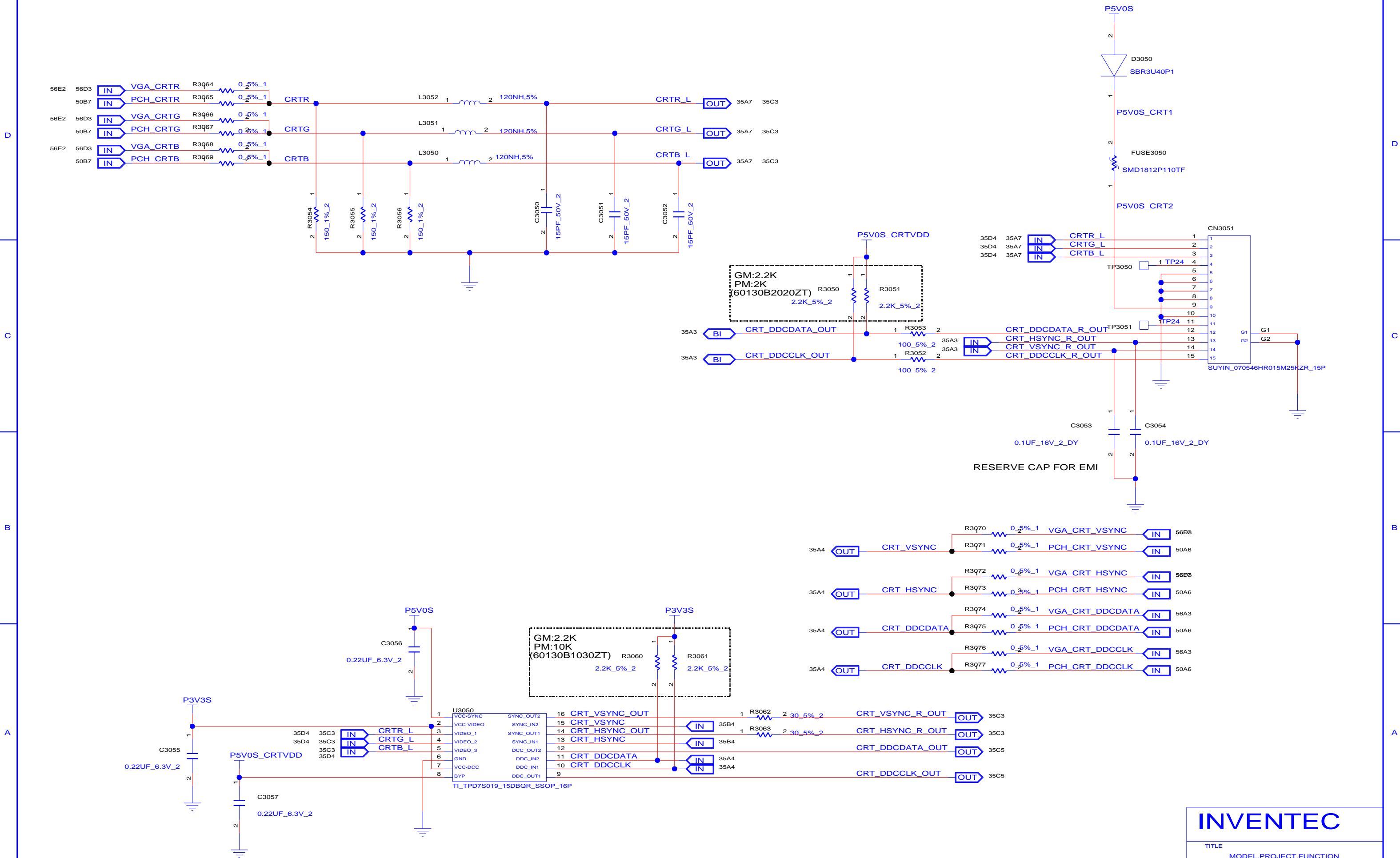
C

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REFERENCE 3050~3099(CRT)



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REFERENCE 3150~3199(HDMI)

D

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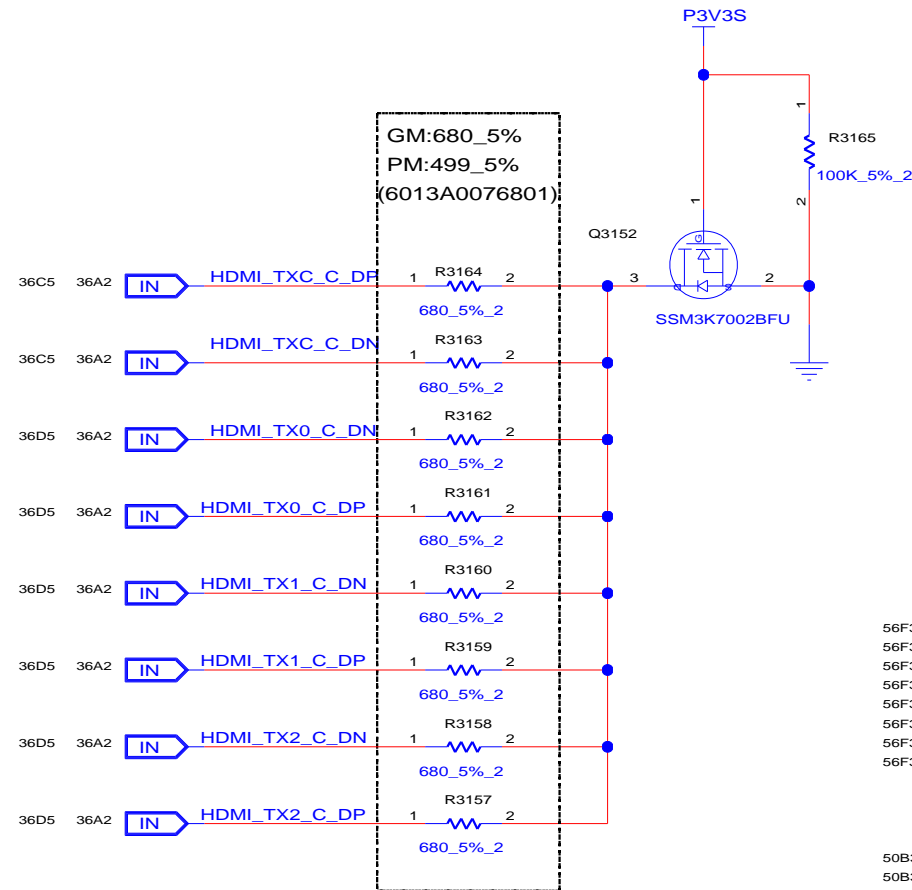
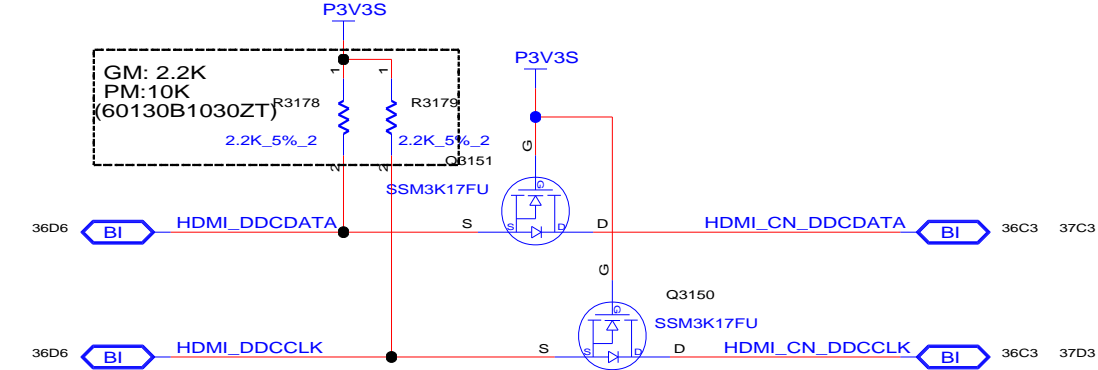
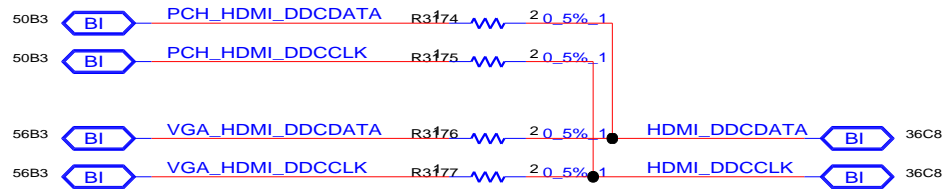
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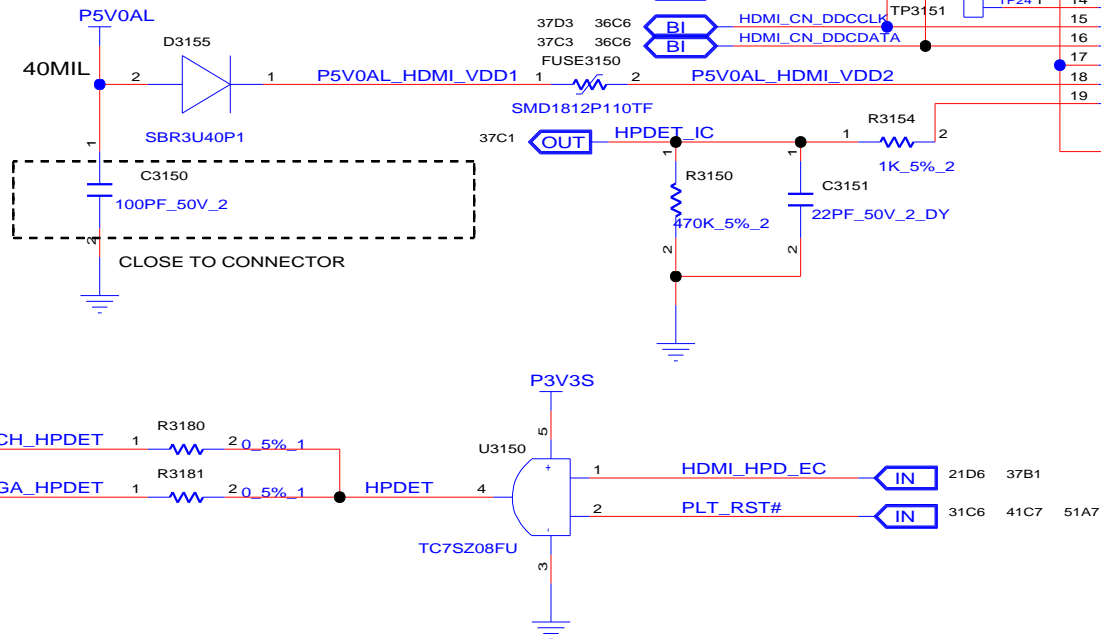
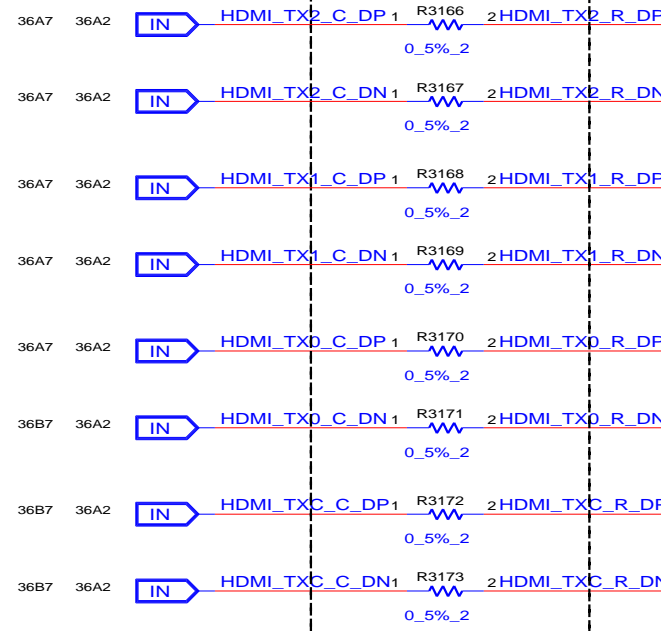
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PLACE CLOSE TO CONNECTOR



INVENTEC

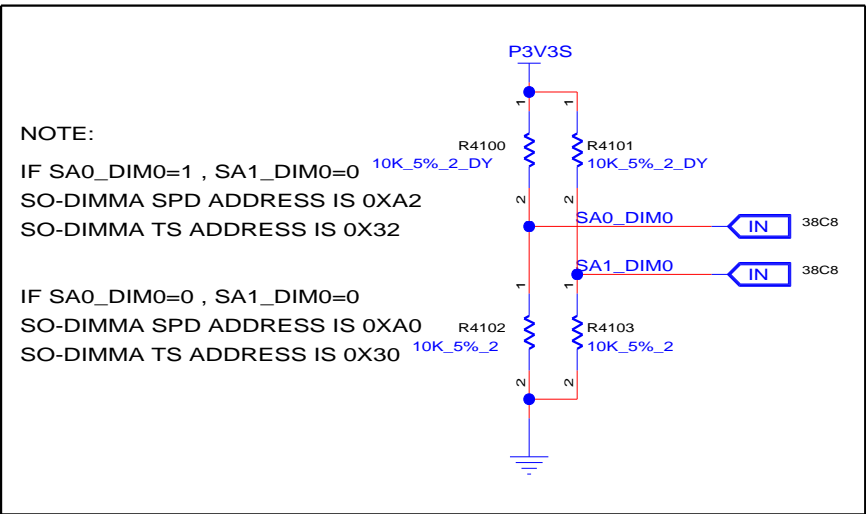
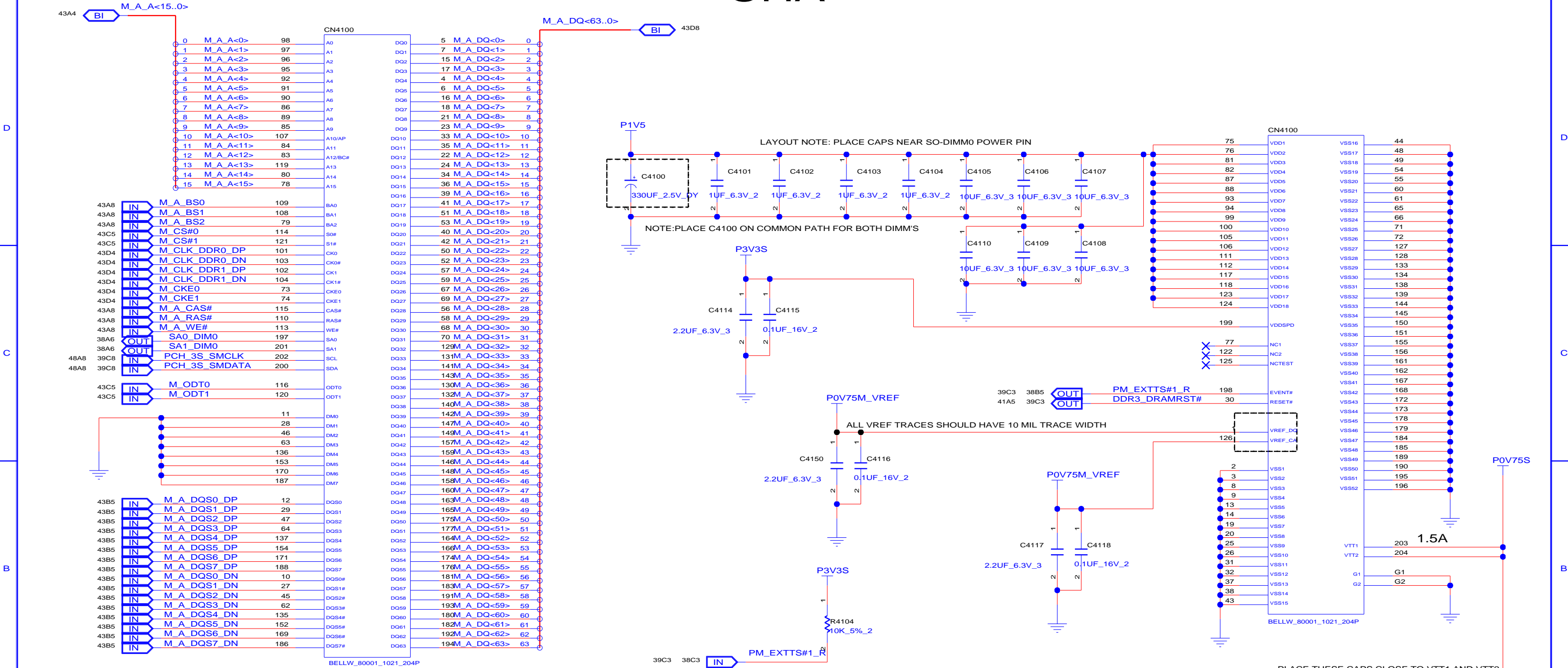
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CHA

REFERENCE 4100~4299(DDR)



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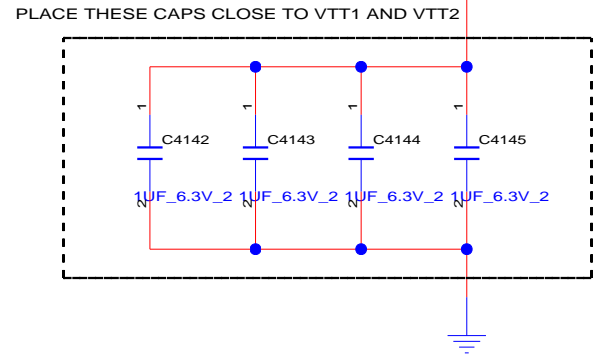
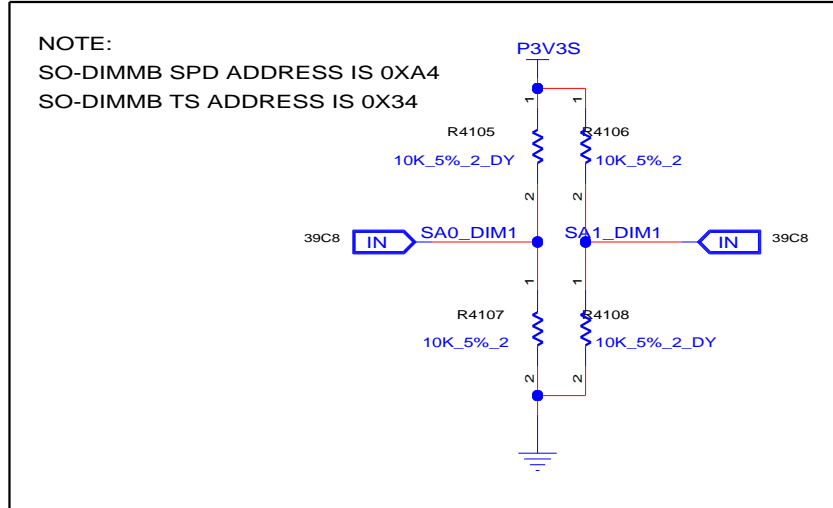
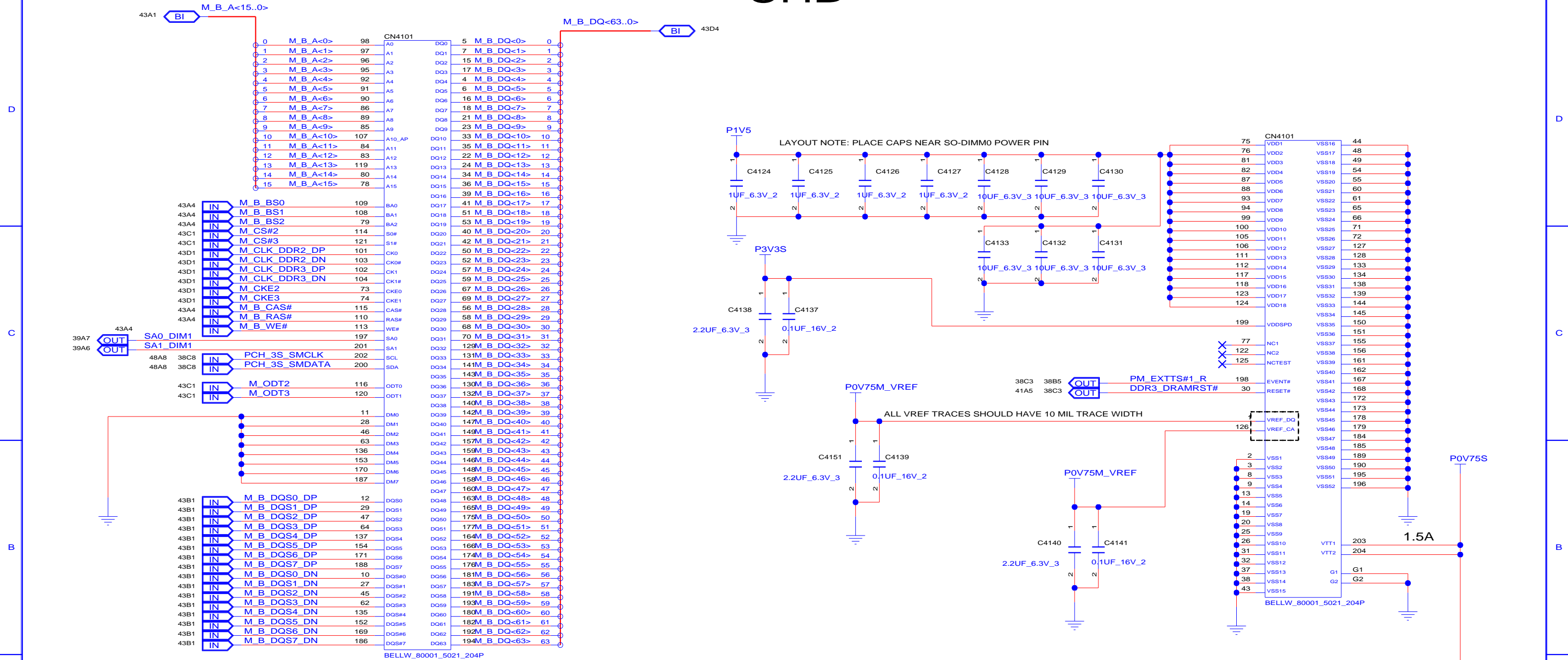
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REFERENCE 4100~4299(DDR)

CHB

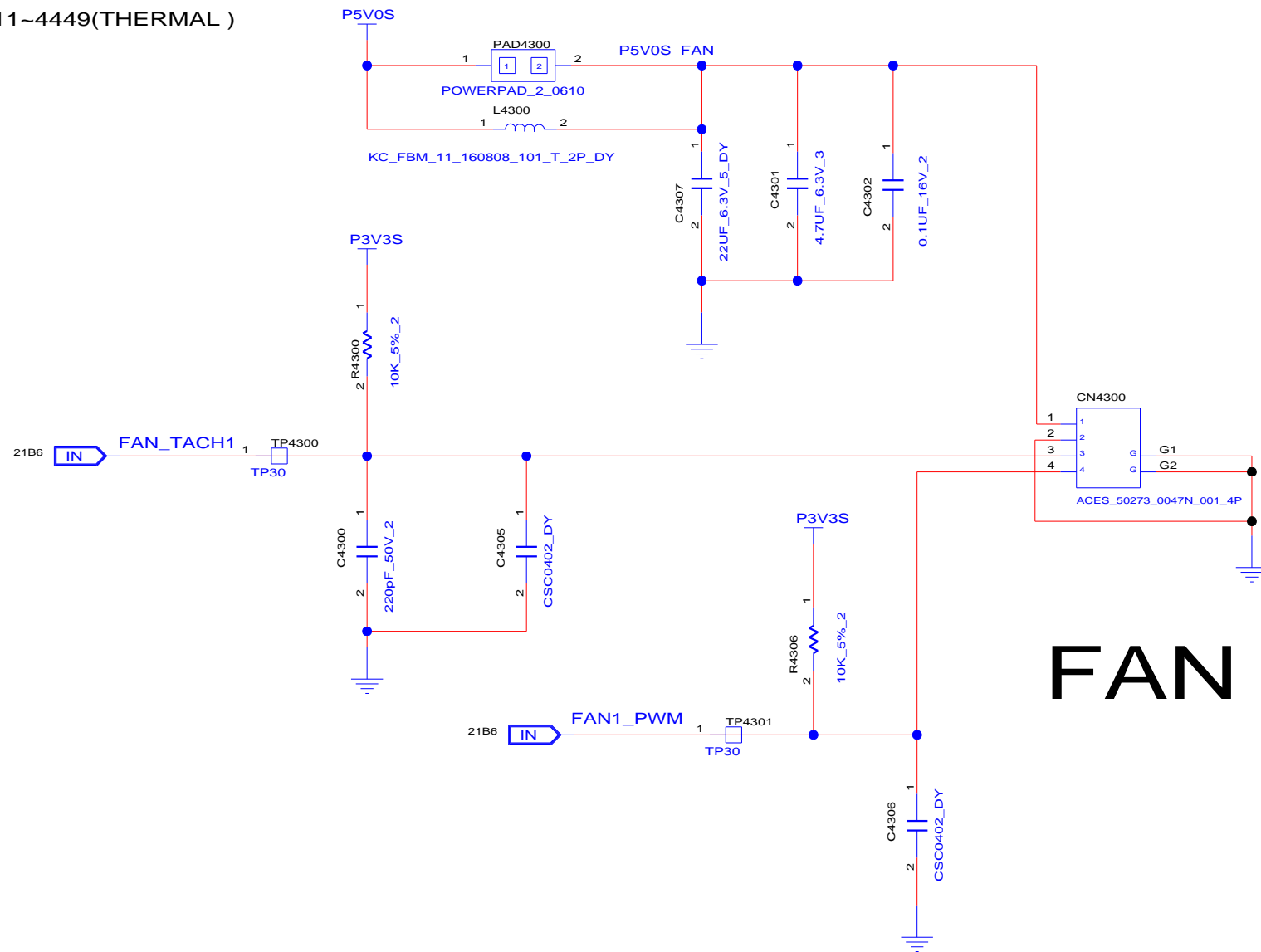


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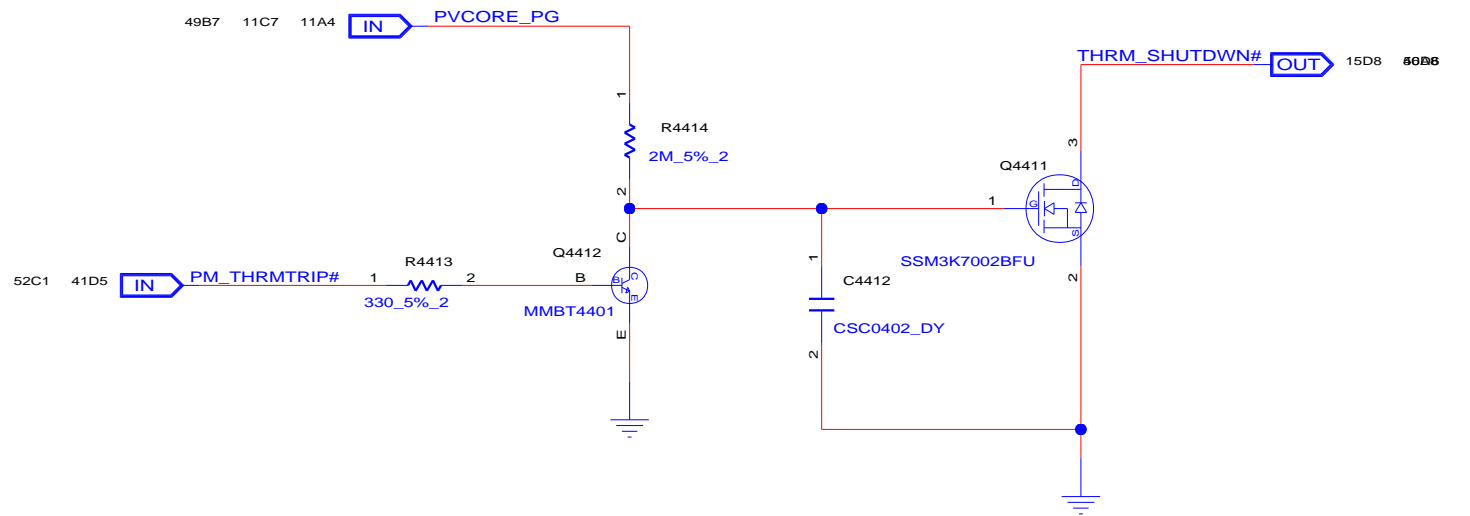
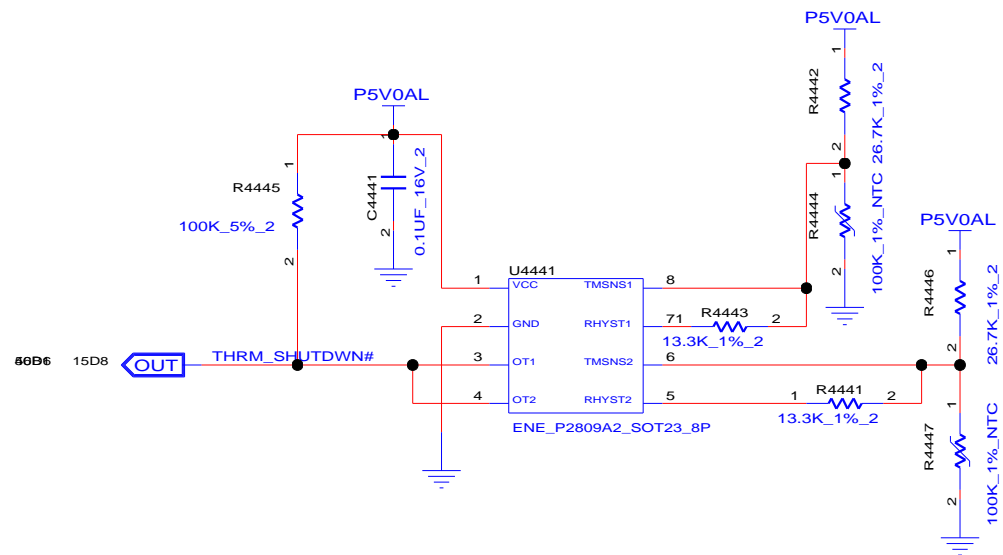
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REFERENCE 4300~4349(FAN)
REFERENCE 4411~4449(THERMAL)



FAN CN

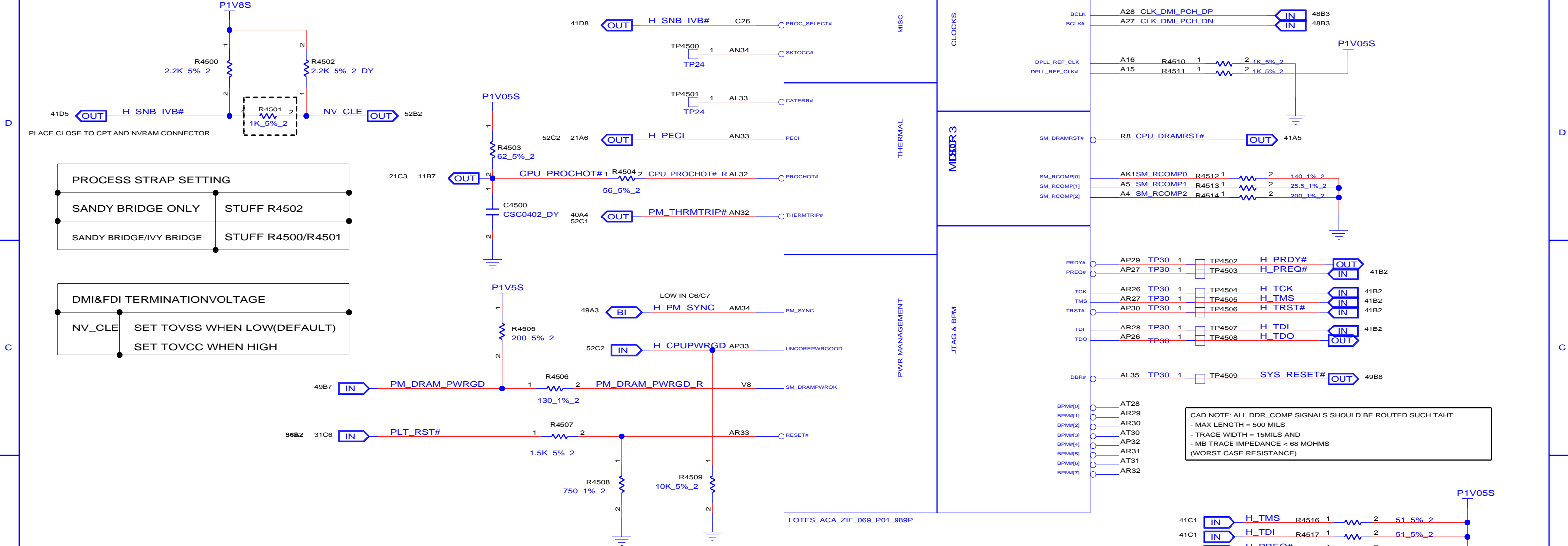


INVENTEC

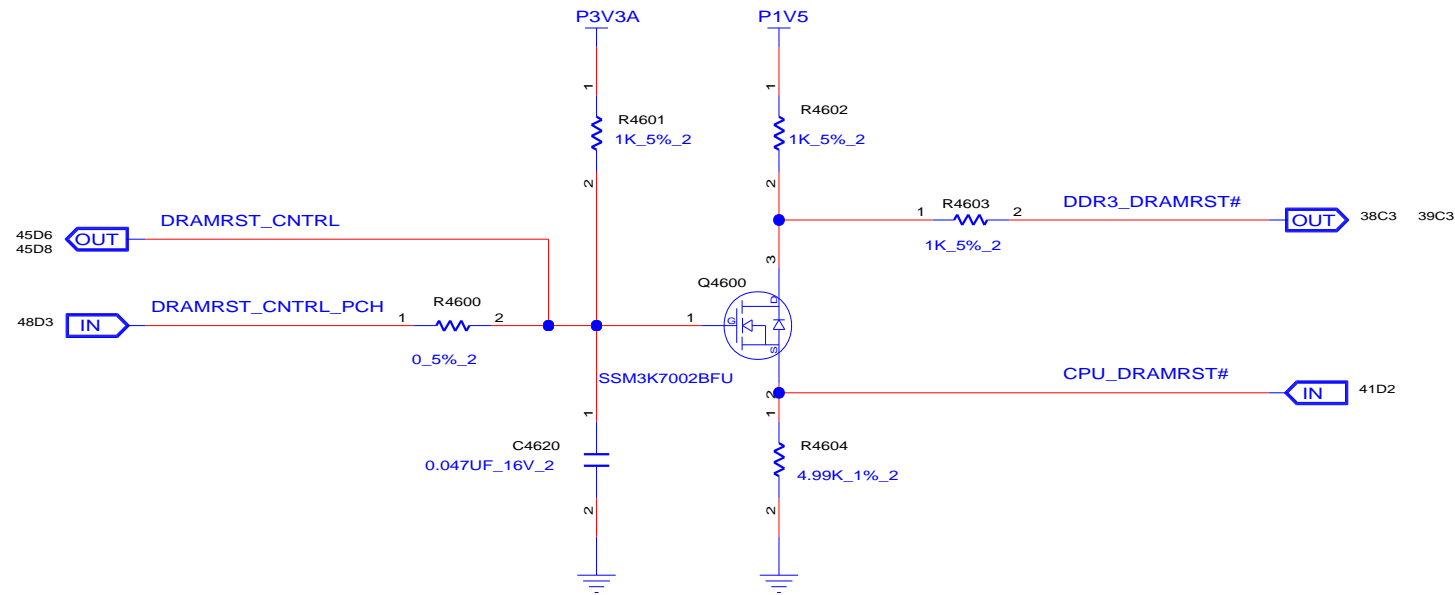
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002 SHEET 40 of 68

REFERENCE 4500~4699(CPU)



S3 CIRCUIT: DRAM_RST# TO MEMORY SHOULD BE HIGH DURING S3



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002 SHEET 41 of 68

REFERENCE 4500~4699(CPU)

P1V05S

R4521
24.9_1%_2

CAD NOTE: DP_COMPIO AND ICOMPO SIGNALS
SHOULD BE SHORTED NEAR BALLS AND ROUTED WITH
- TYPICAL IMPEDANCE < 25 MOHMS

CAD NOTE: PEG_ICOMPI AND RCOMPO SIGNALS
SHOULD BE SHORTED AND ROUTED WITH
- MAX LENGTH = 500 MILS
- TYPICAL IMPEDANCE = 43 MOHMS

PEG_ICOMPO SIGNALS SHOULD BE ROUTED WITH
- MAX LENGTH = 500 MILS
- TYPICAL IMPEDANCE = 14.5 MOHMS

CLOSE TO CPU

INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE A3 CODE CS DOC.NUMBER 1310xxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002 SHEET 42 of 68

REFERENCE 4500~4699(CPU)

SOCKET,CPU,989P,TIN,3.0MM,STR,SMD,TR

D

C

B

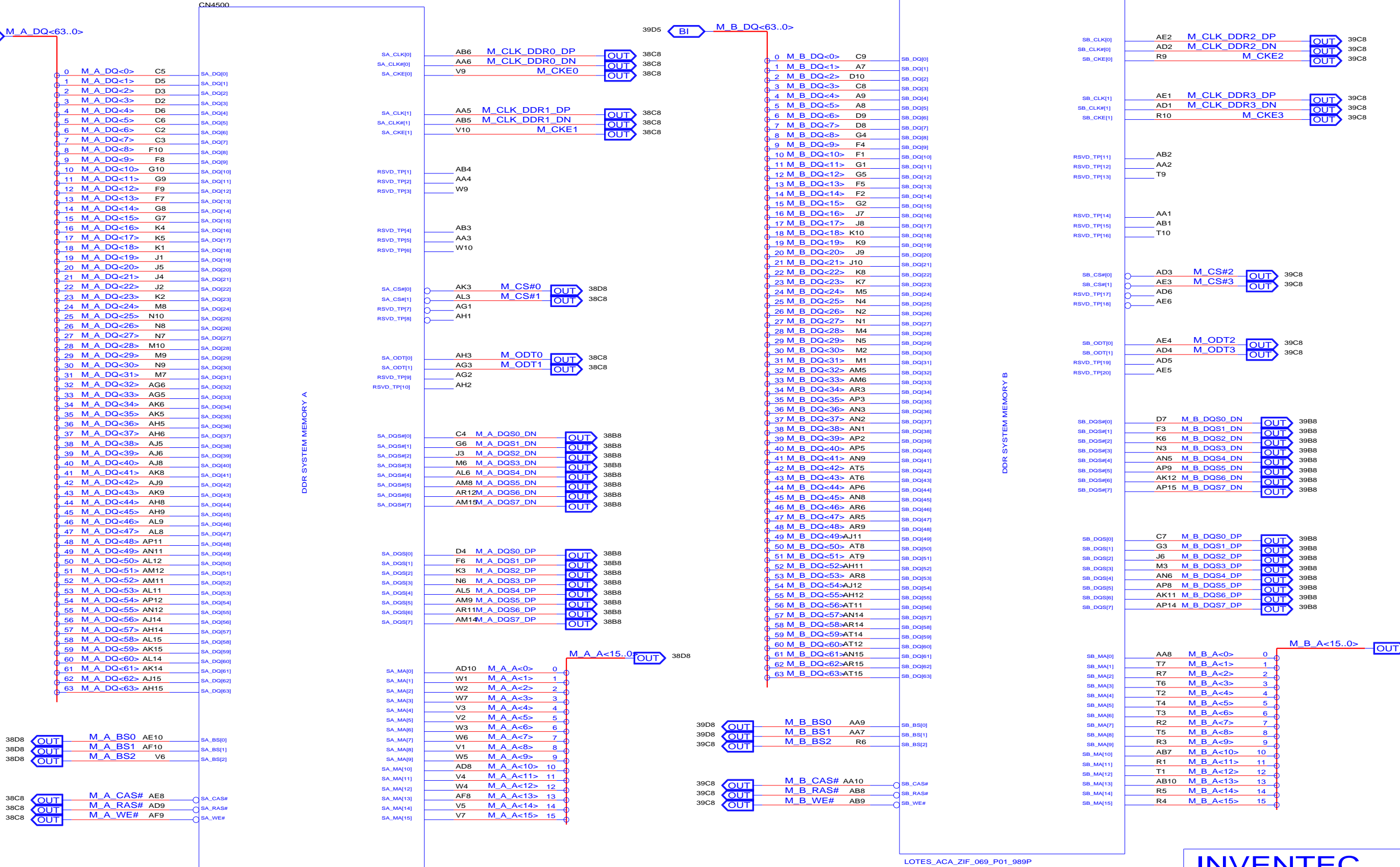
A

D

C

B

A

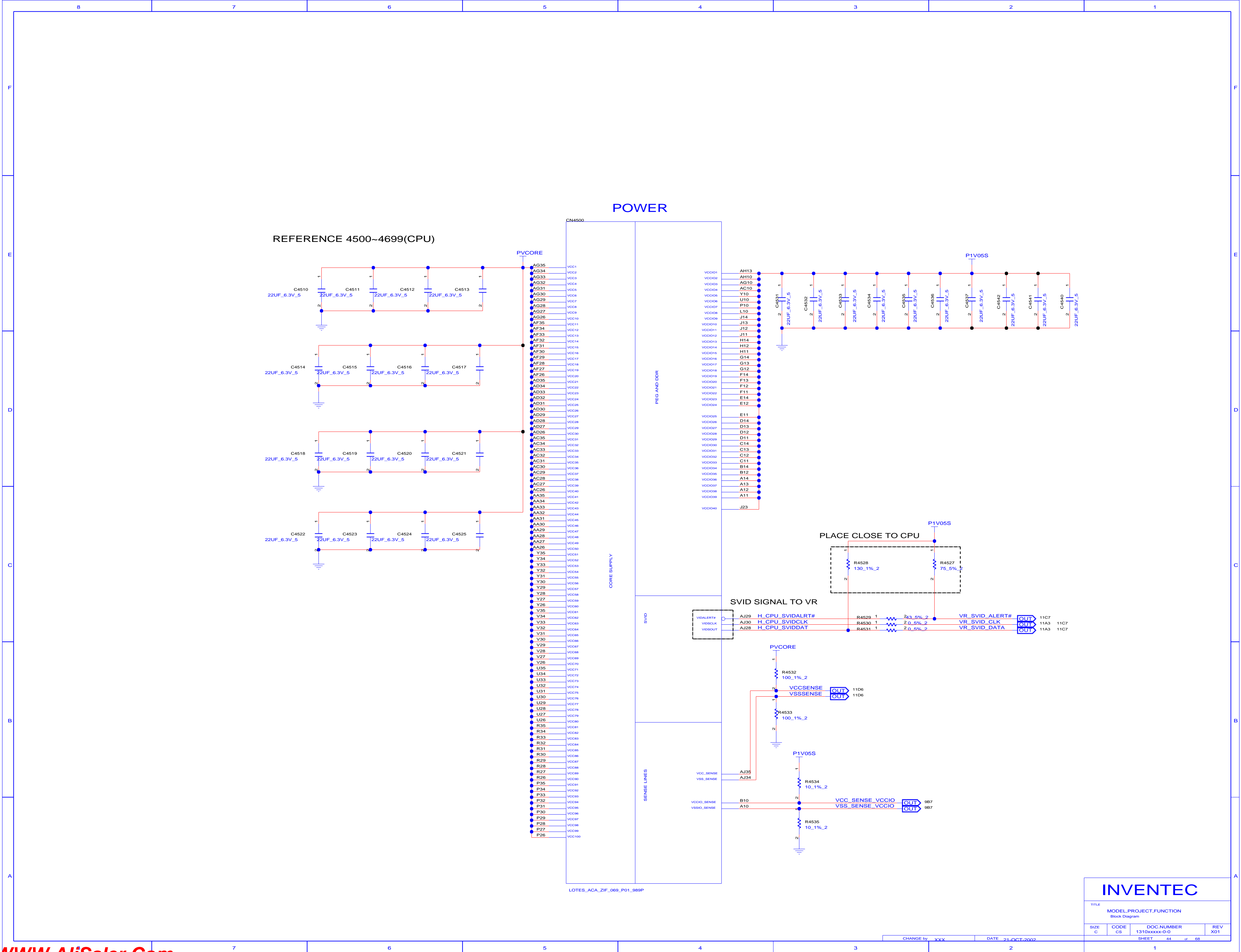


INVENTEC

TITLE MODEL,PROJECT,FUNCTION Block Diagram

SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002 SHEET 43 of 68



POWER

SENSE LINES

VREF

DDR3 -1.5V RAILS

SA RAIL

MISC

1.8V RAIL

LOTES_ACA_ZIF_069_P01_989P

CHANGE by XXX

DATE 21-OCT-2002

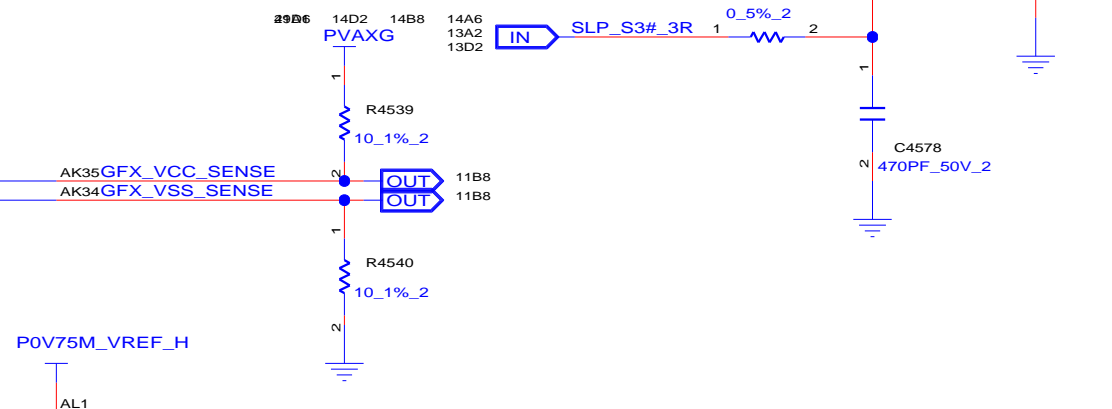
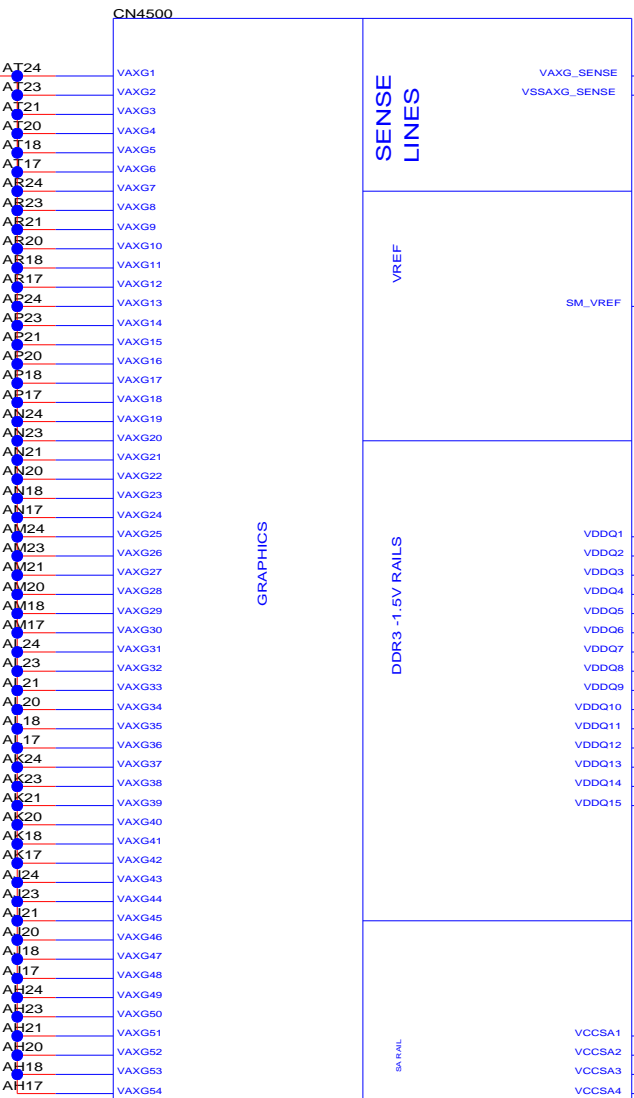
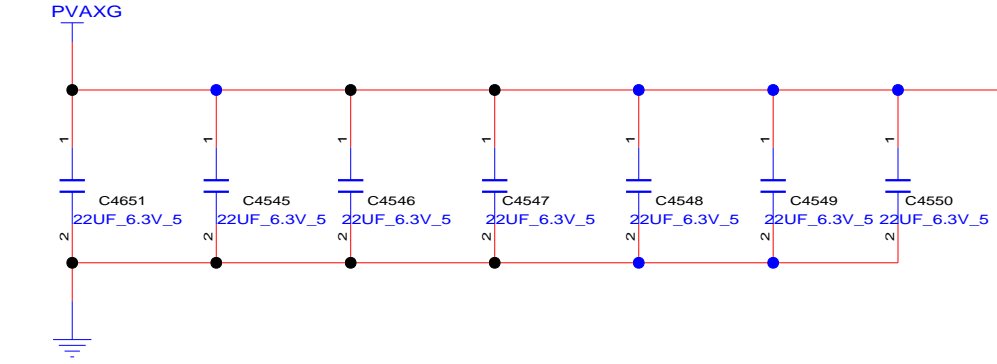
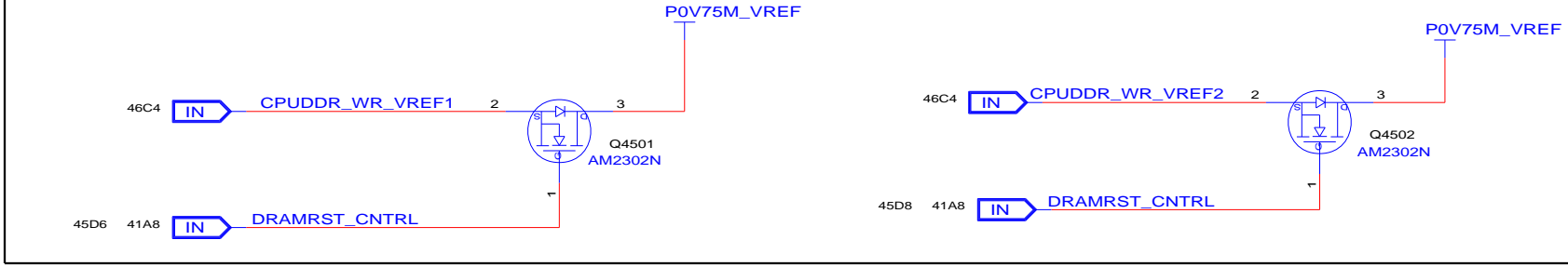
SHEET 45 of 68

INVENTEC

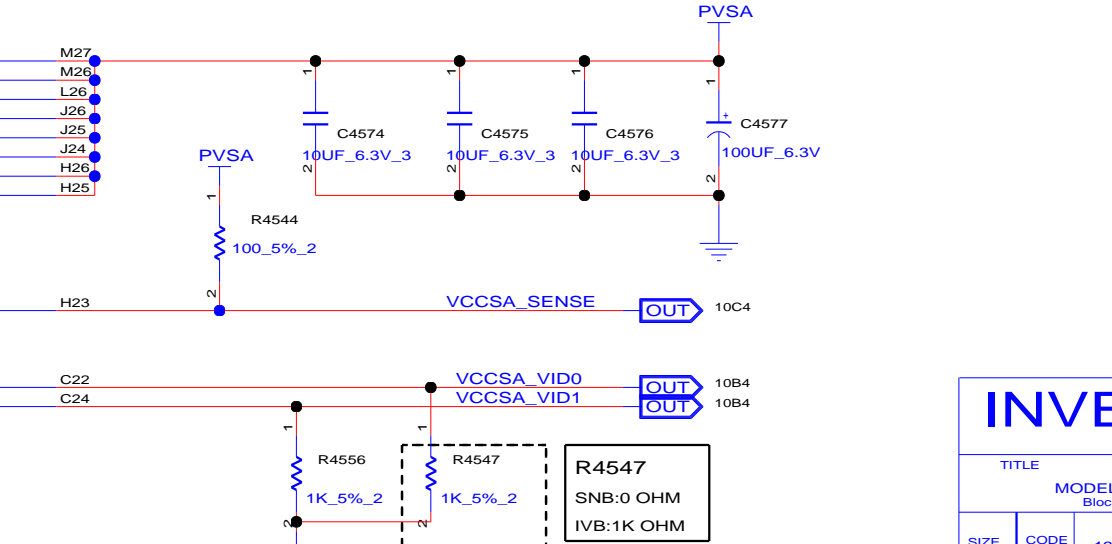
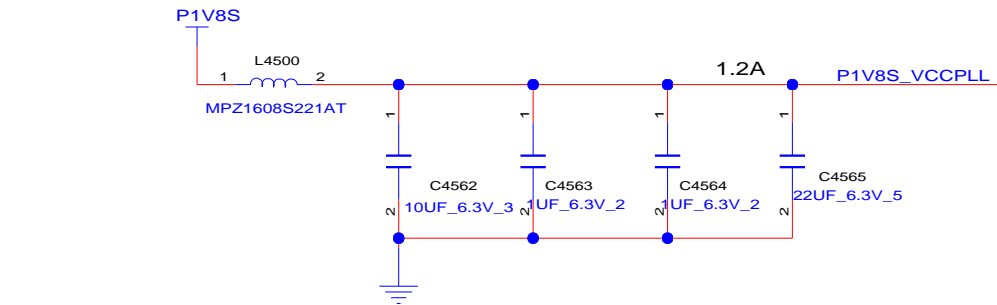
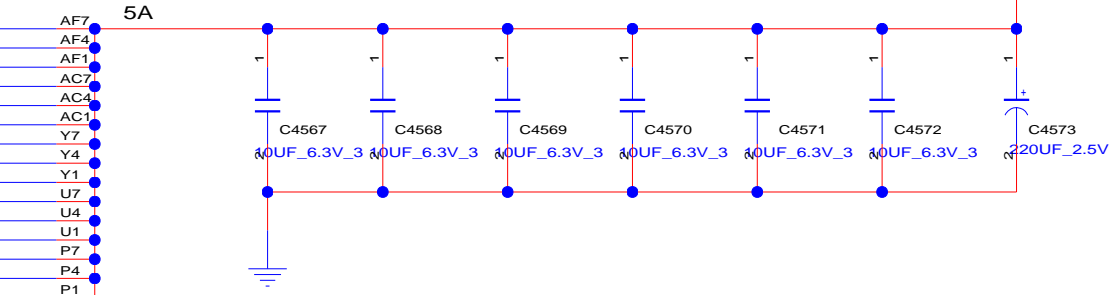
TITLE MODEL,PROJECT,FUNCTION
Block Diagram

SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0	REV X01
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PROCESSOR DRIVEN VREF PATH WAS STUFFED BY DEFAULT:
ROUTE WITH MIN. TRACE WIDTH OF 10 MILS

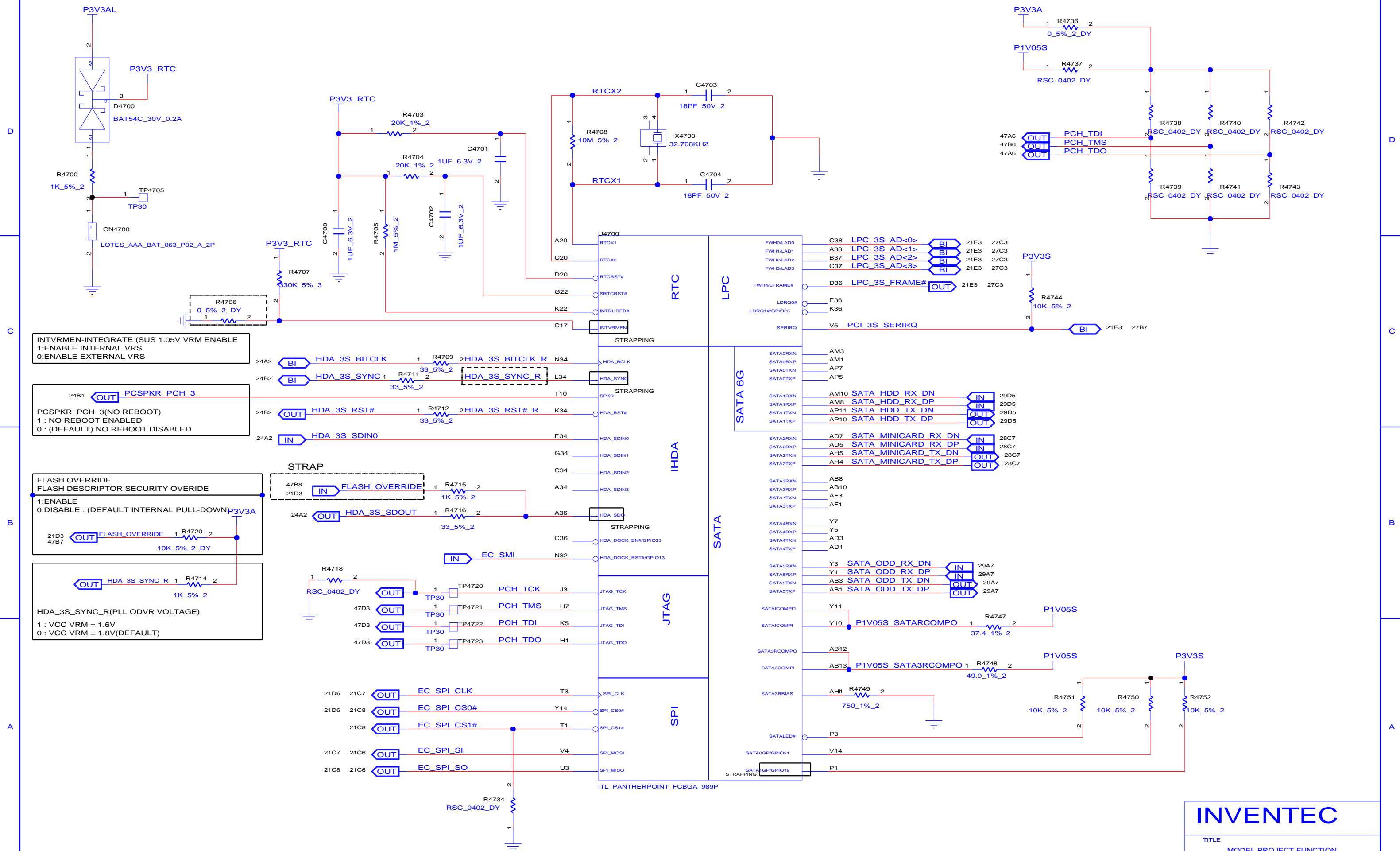


NOTE : DDR_WR_VREF SHOULD HAVE 20/20 MIL WHEREVER POSSIBLE





REFERENCE 4700~4949(PCH)



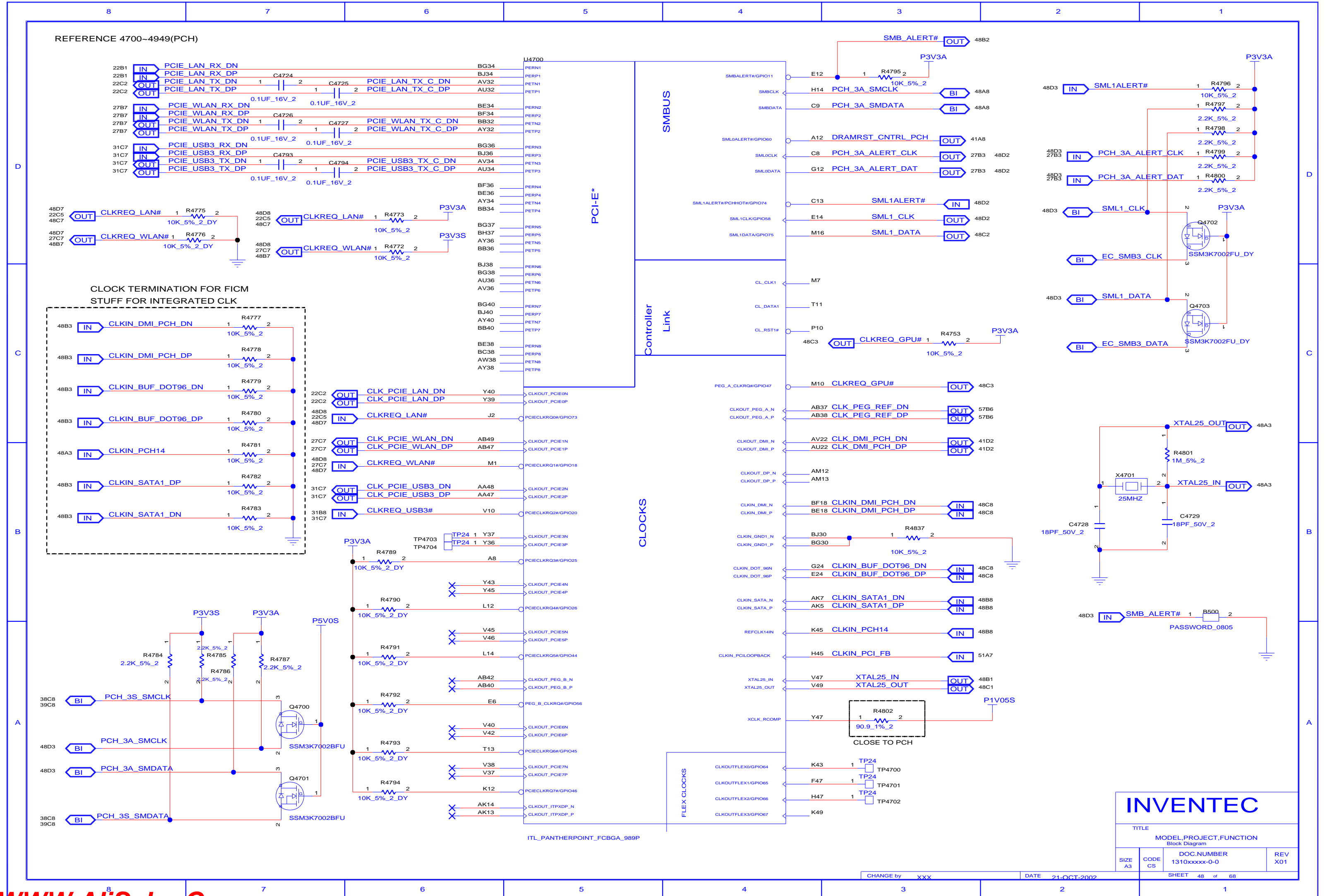
INVENTEC

TITLE	MODEL PROJECT FUNCTION
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SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0
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CHANGE by	XXX	DATE	21-OCT-2002
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SHEET 47 of 68

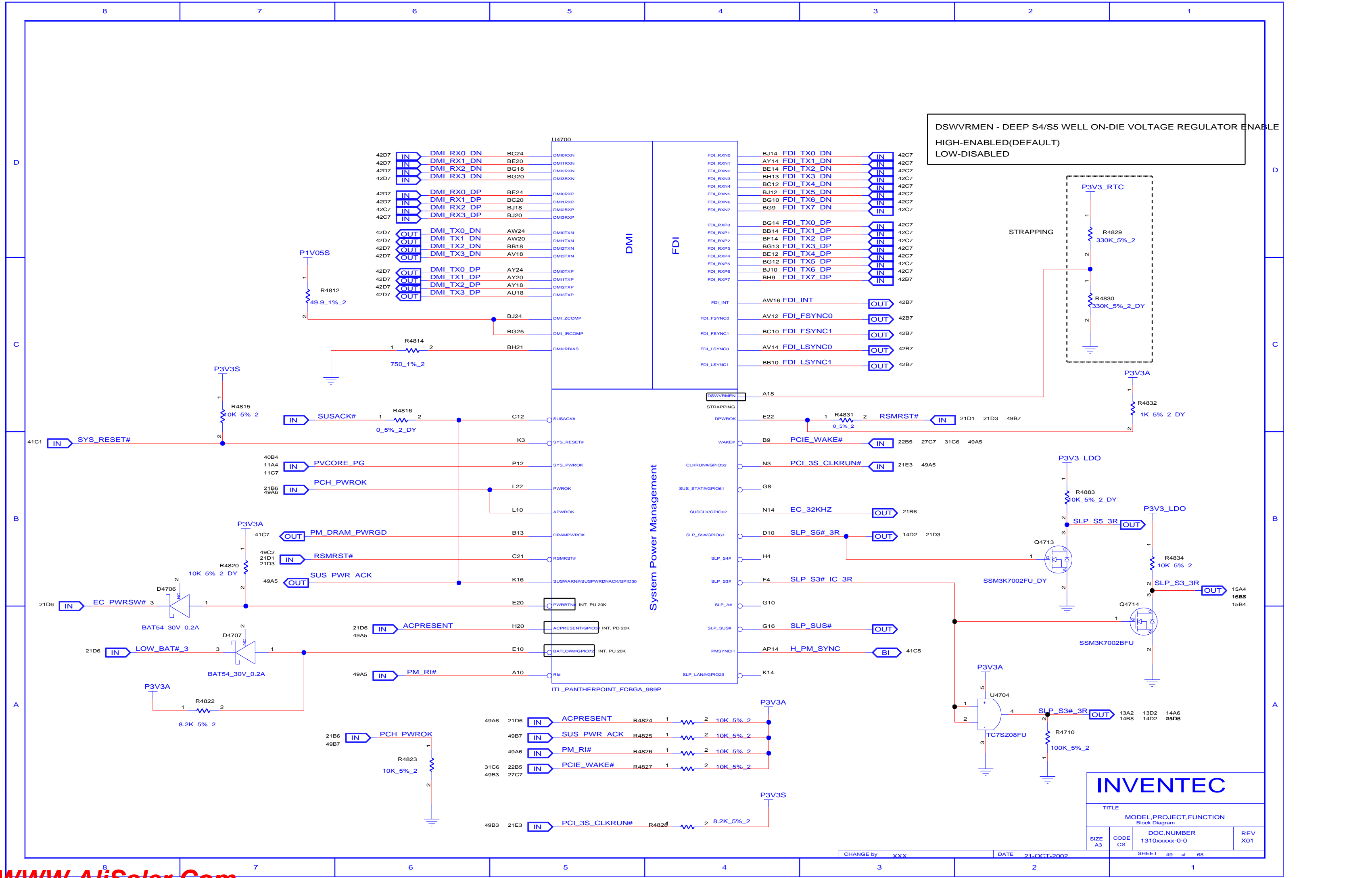


INVENTEC

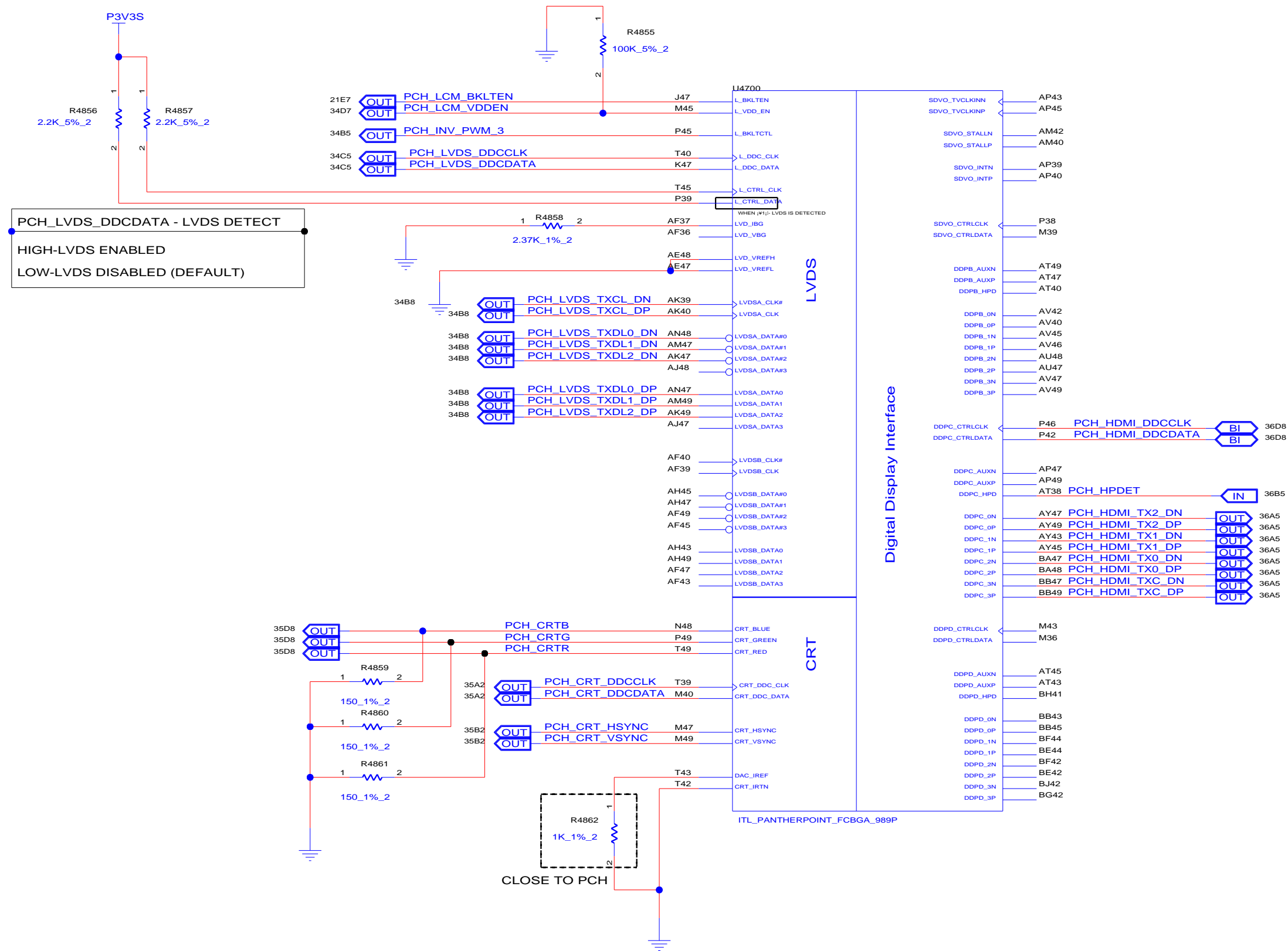
TITLE MODEL,PROJECT,FUNCTION Block Diagram

SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01

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REFERENCE 4700~4949(PCH)



INVENTEC

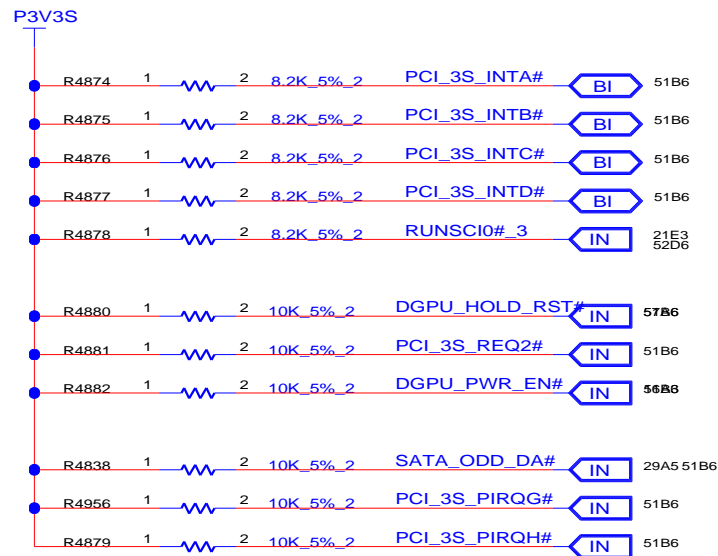
TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0	REV X01
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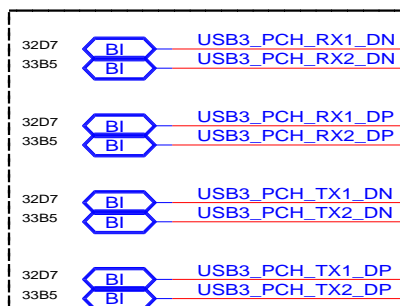
CHANGE by XXX DATE 21-OCT-2002 SHEET 50 of 68

REFERENCE 4700~4949(PCH)

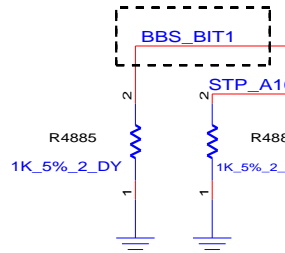
GPIO51 BBS_BIT1	GPIO19 BBS_BIT0	BOOT BIOS DESTINATION
0	1	RESERVED(NAND)
1	0	-----
1	1	SPI (DEFAULT)
0	0	LPC



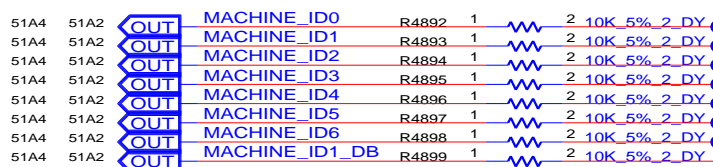
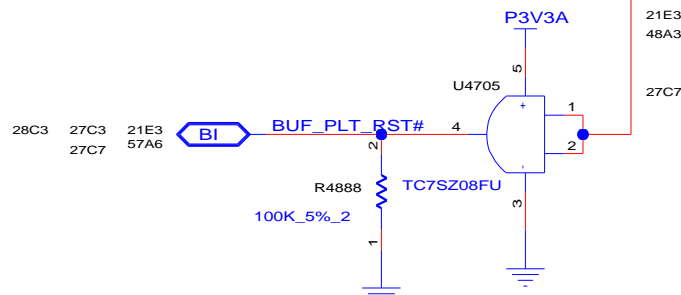
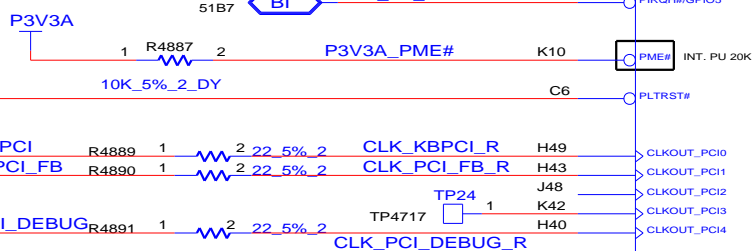
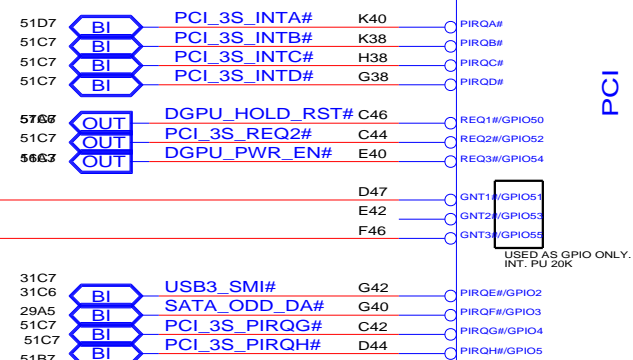
ROUTE WITH 90 OHMS IMPEDANCE
TOTAL LENGTH NO LONGER THAN 11 INCHES



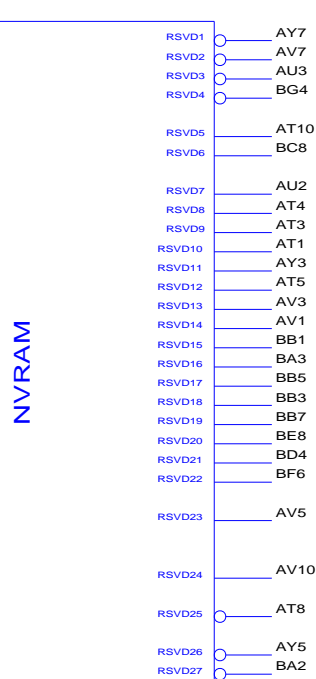
BBS STRAPING



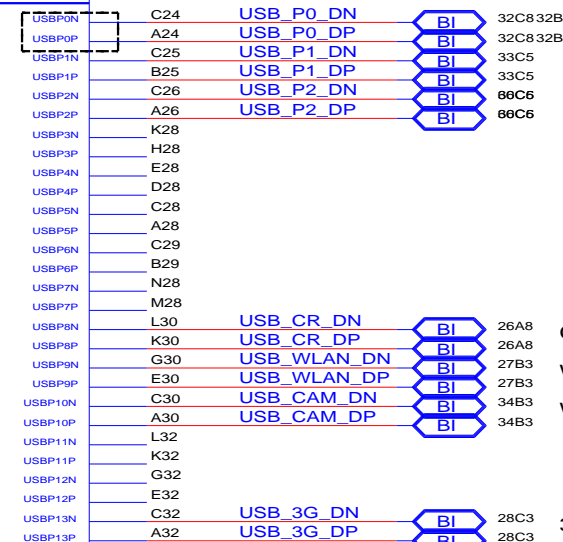
LOW=A16 SWAP OVERRIDE
STP_A16OVR
TOP-BLOCK SWAP OVERRIDE
HIGH=DEFAULT



NOTE:10K_5%(60130B1030ZT)



DEBUG PORT

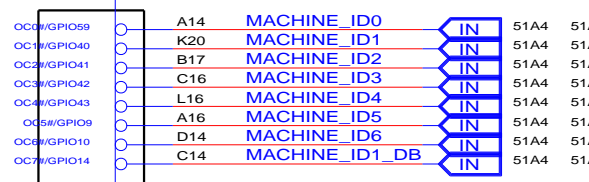


NOTE:
USB2.0/3.0 COMBO-USB2.0 PORT 0,1 MAPPED USB3.0 PORT 1,2

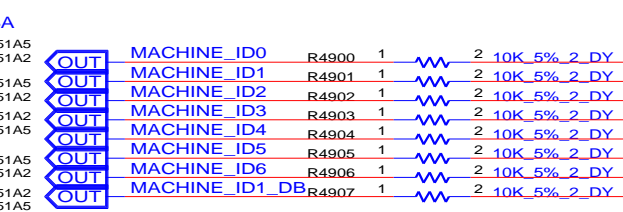
P0,P1 RESERVER FOR USB3.0

CARD READER
WLAN
WEBCAM

3G



TO BE USED AS GPIO



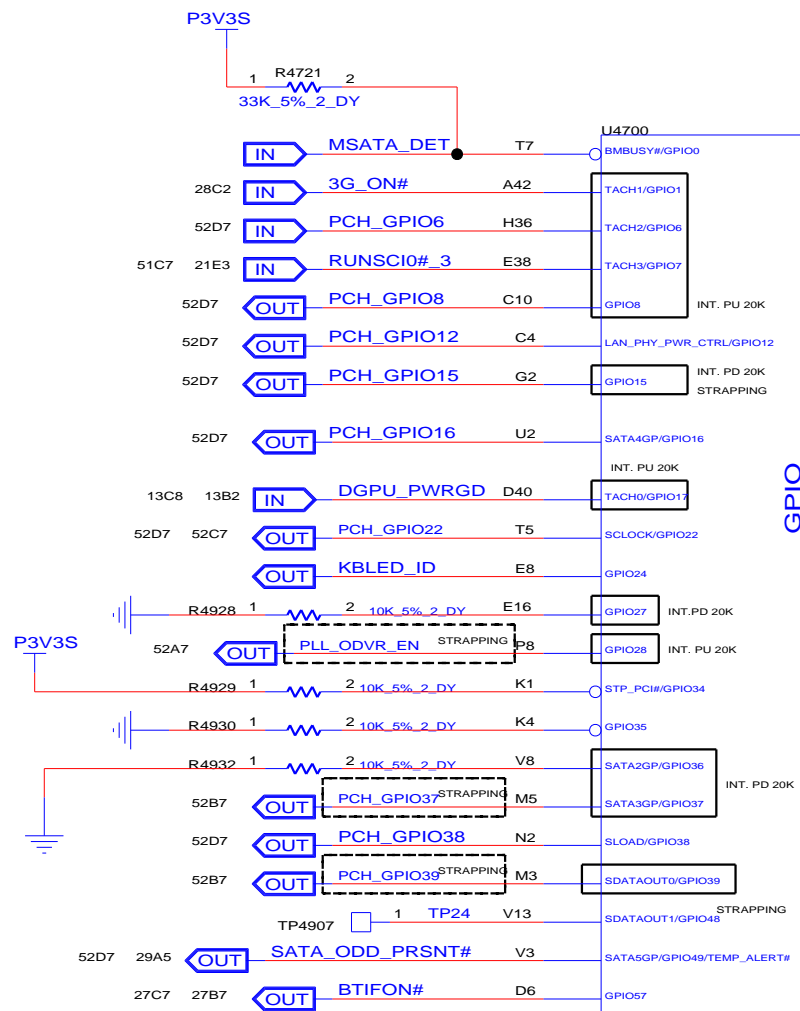
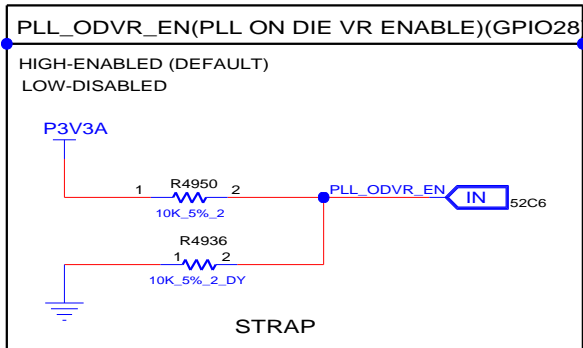
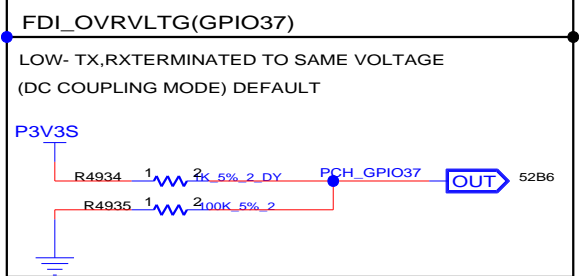
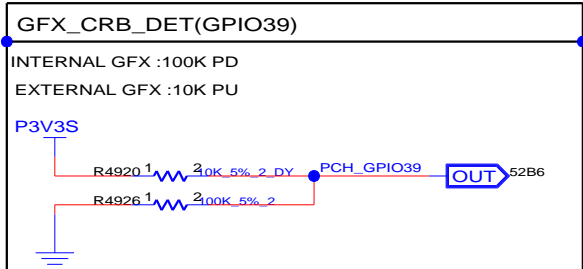
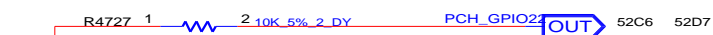
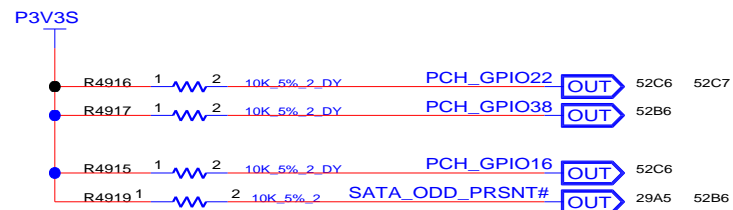
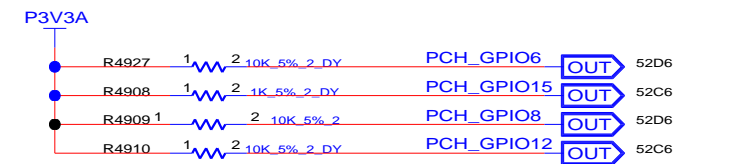
NOTE:10K_5%(60130B1030ZT)

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01

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REFERENCE 4700~4949(PCH)



GPIO

NCTF

A4 VSS_NCTF_1

A44 VSS_NCTF_2

A45 VSS_NCTF_3

A46 VSS_NCTF_4

A5 VSS_NCTF_5

A6 VSS_NCTF_6

B3 VSS_NCTF_7

B47 VSS_NCTF_8

BD1 VSS_NCTF_9

BD49 VSS_NCTF_10

BE1 VSS_NCTF_11

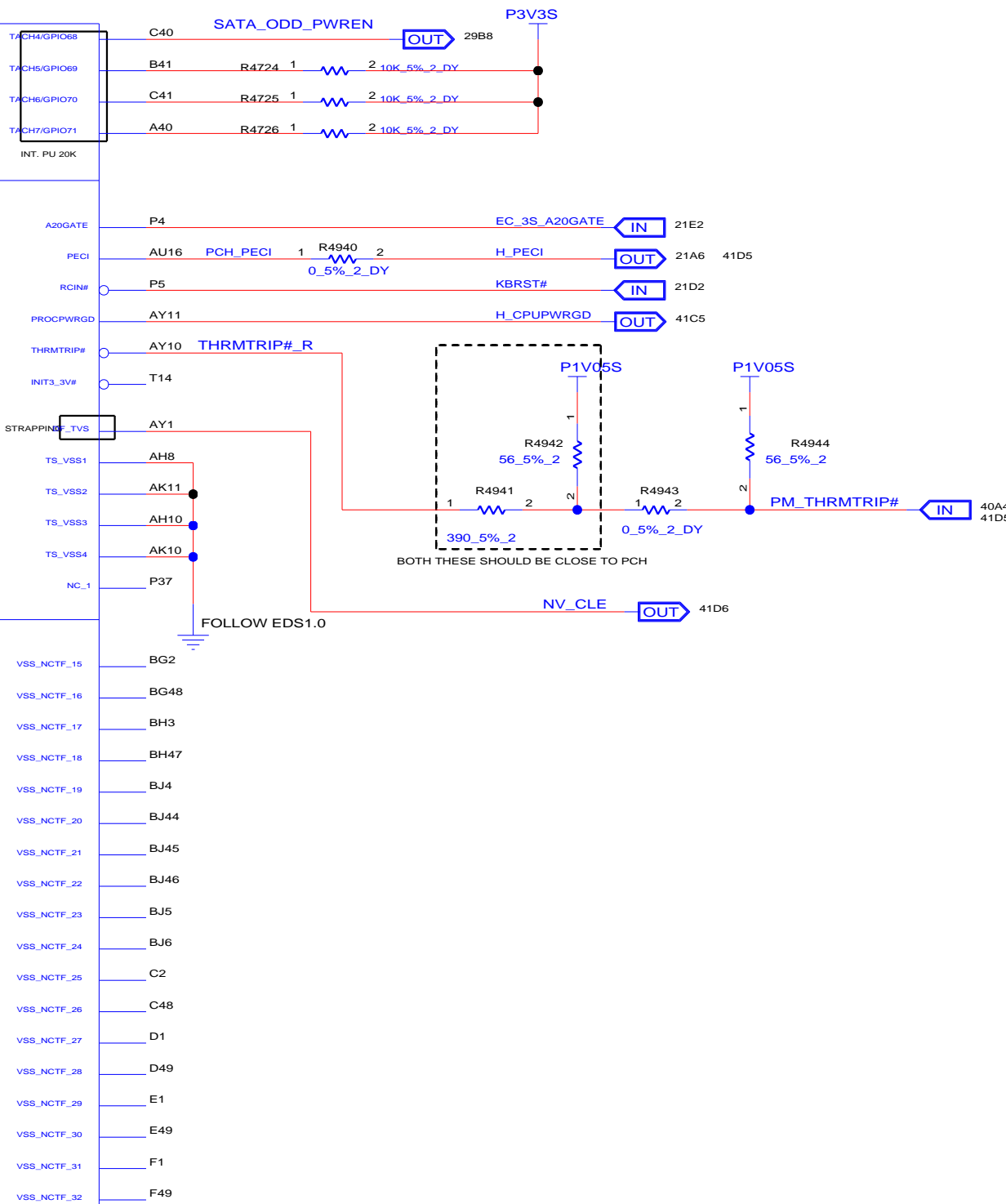
BE49 VSS_NCTF_12

BF1 VSS_NCTF_13

BF49 VSS_NCTF_14

ITL_PANTHERPOINT_FCBGA_989P

CPU/MISC



INVENTEC

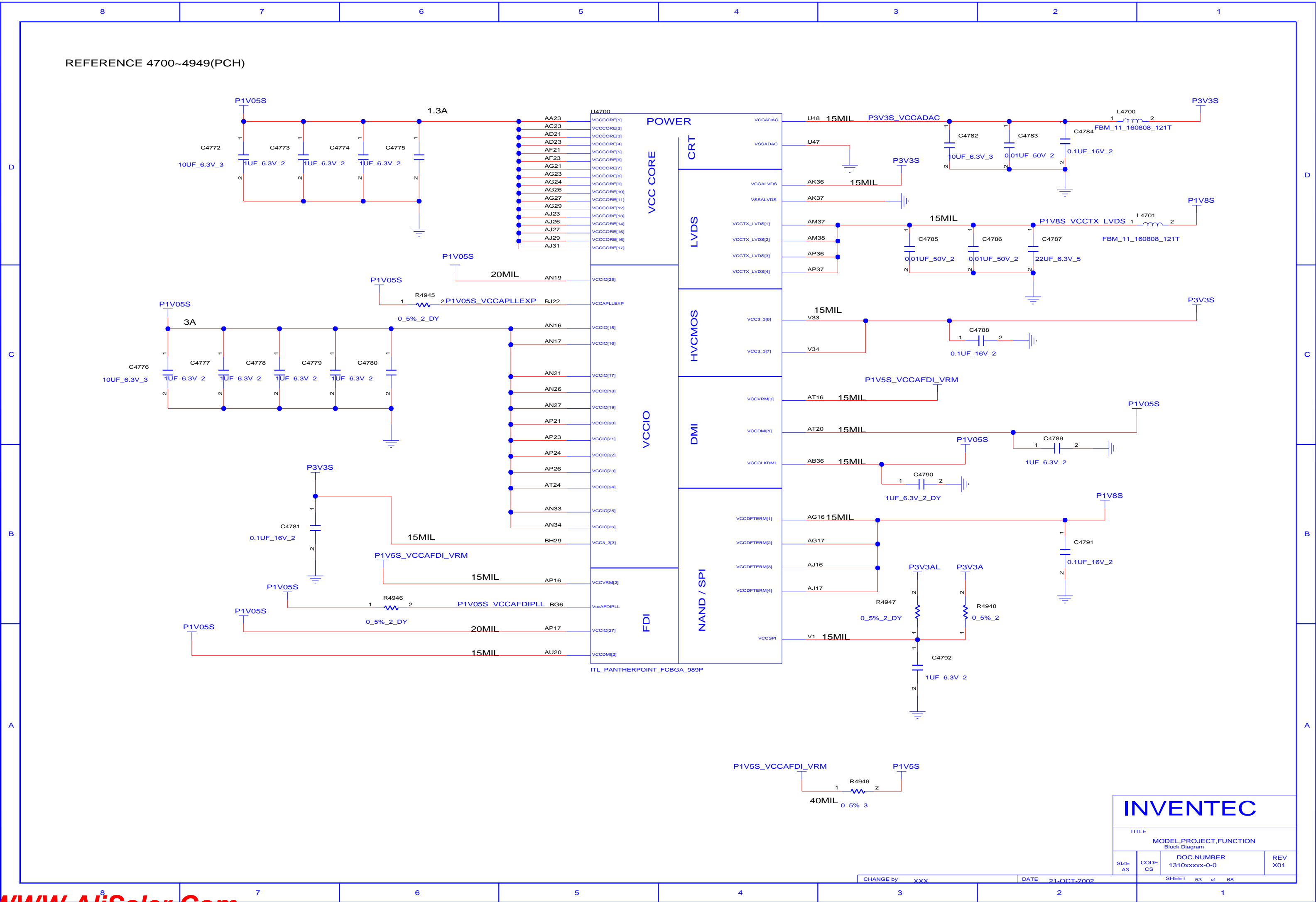
TITLE

MODEL,PROJECT,FUNCTION

Block Diagram

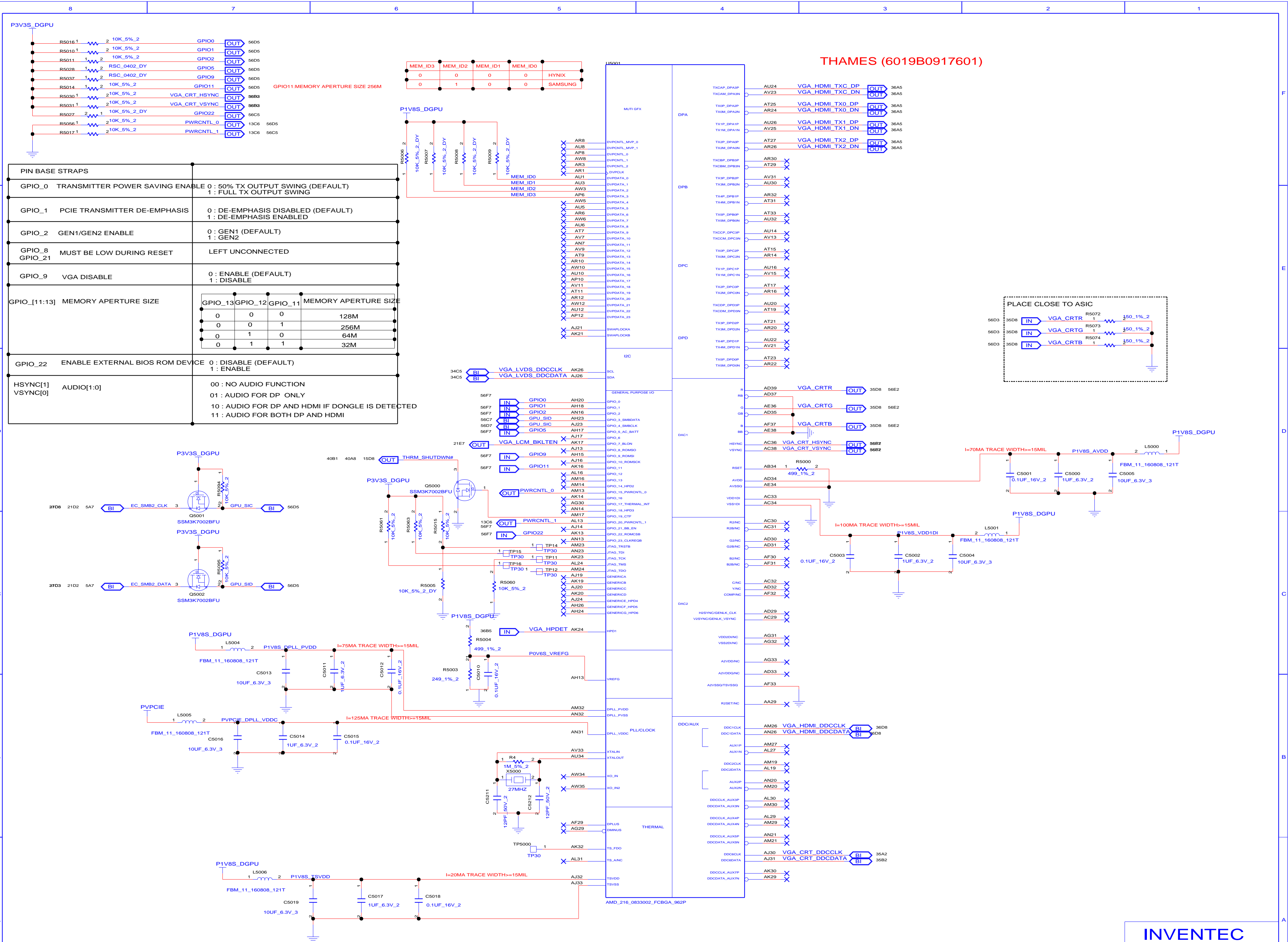
SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0	REV X01
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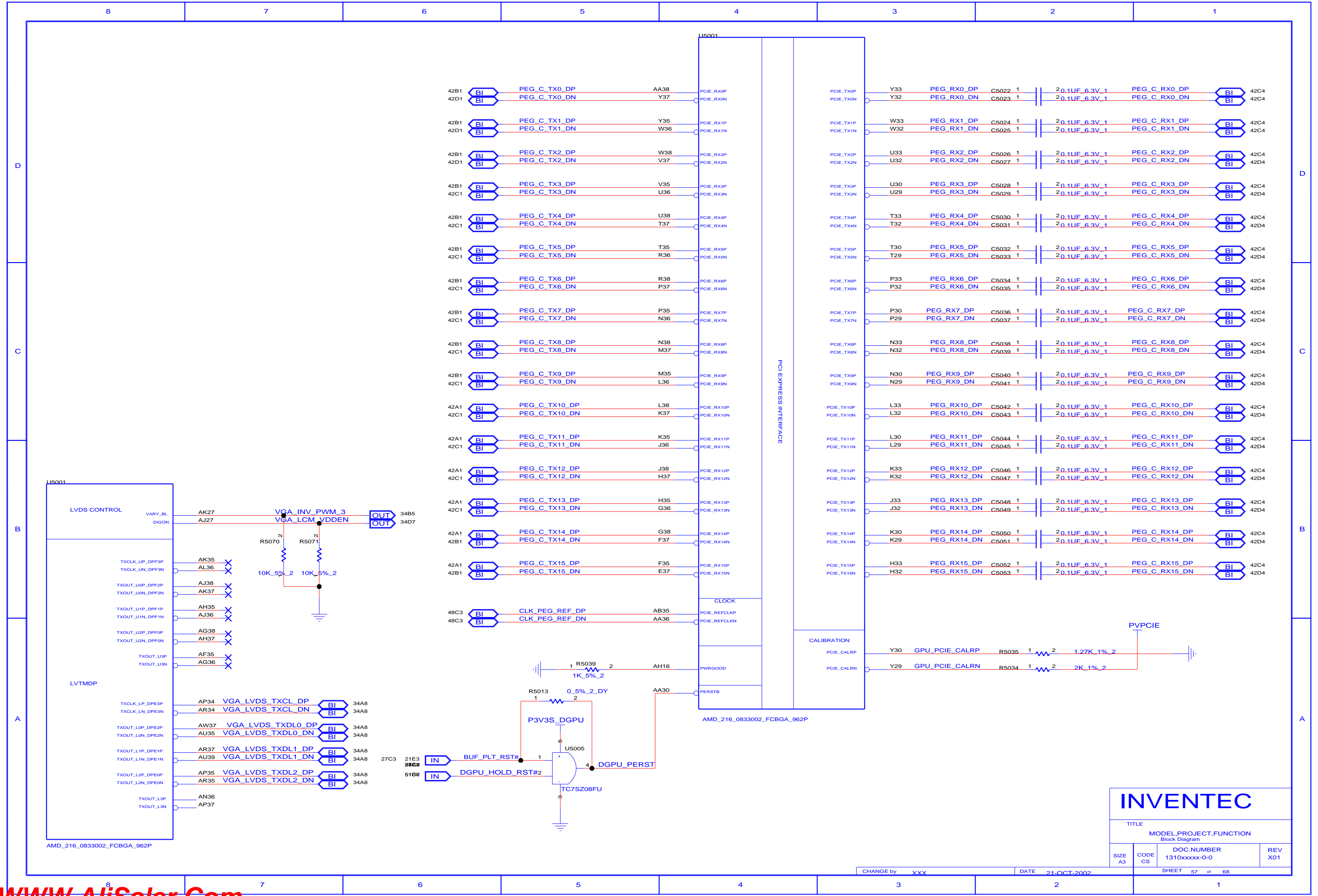
CHANGE by XXX DATE 21.OCT-2002 SHEET 52 of 68



INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01
CHANGE by		DATE	SHEET
XXX		21-OCT-2002	53 of 68





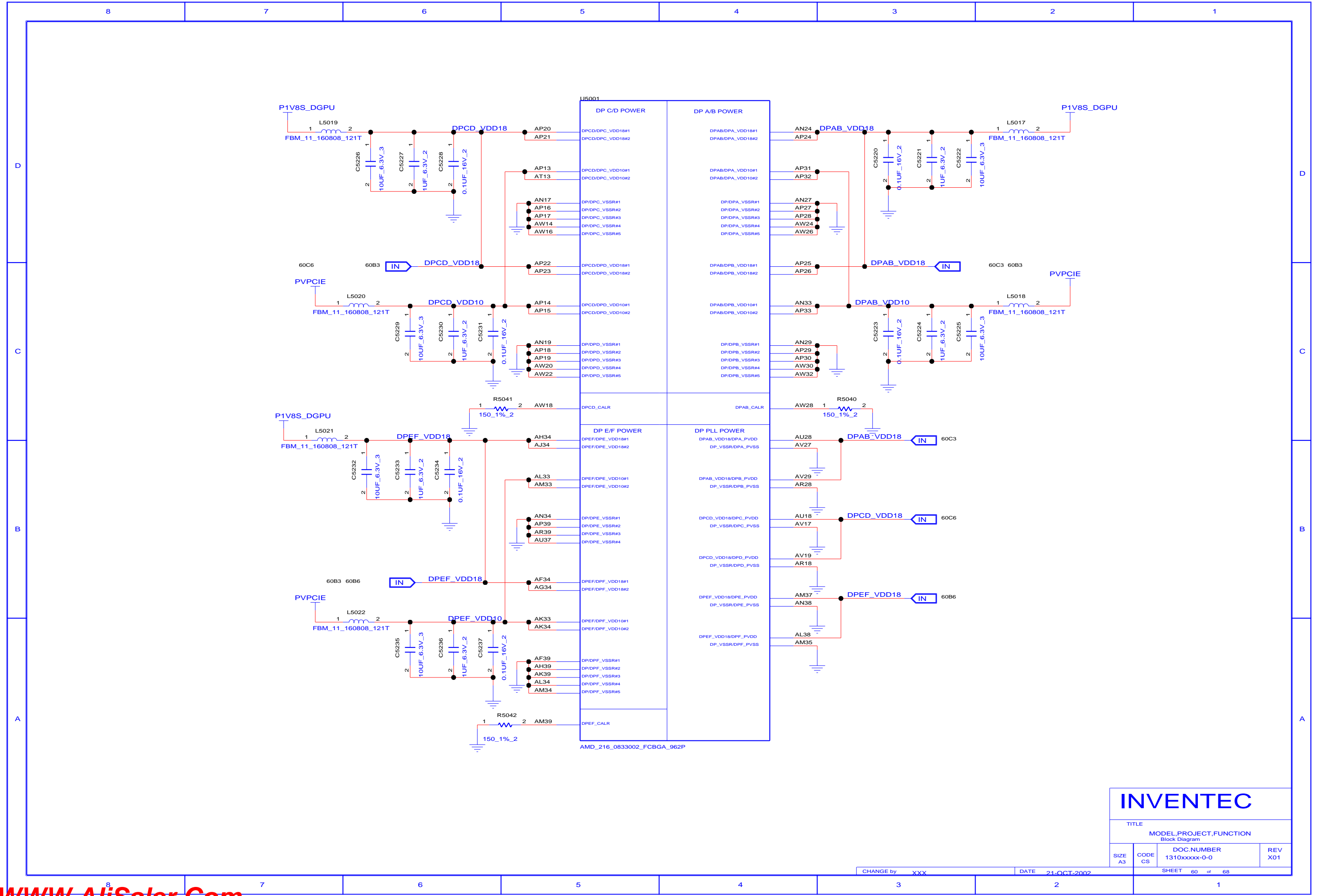


INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002 SHEET 57 of 68

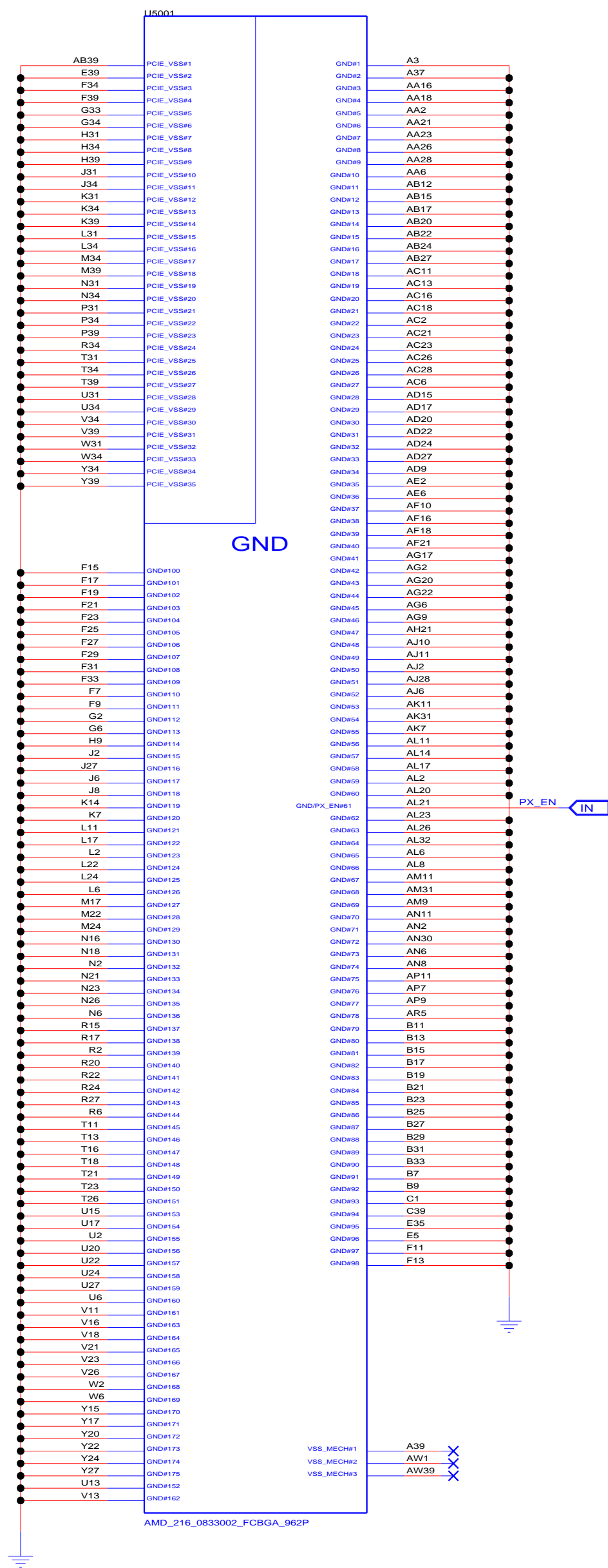


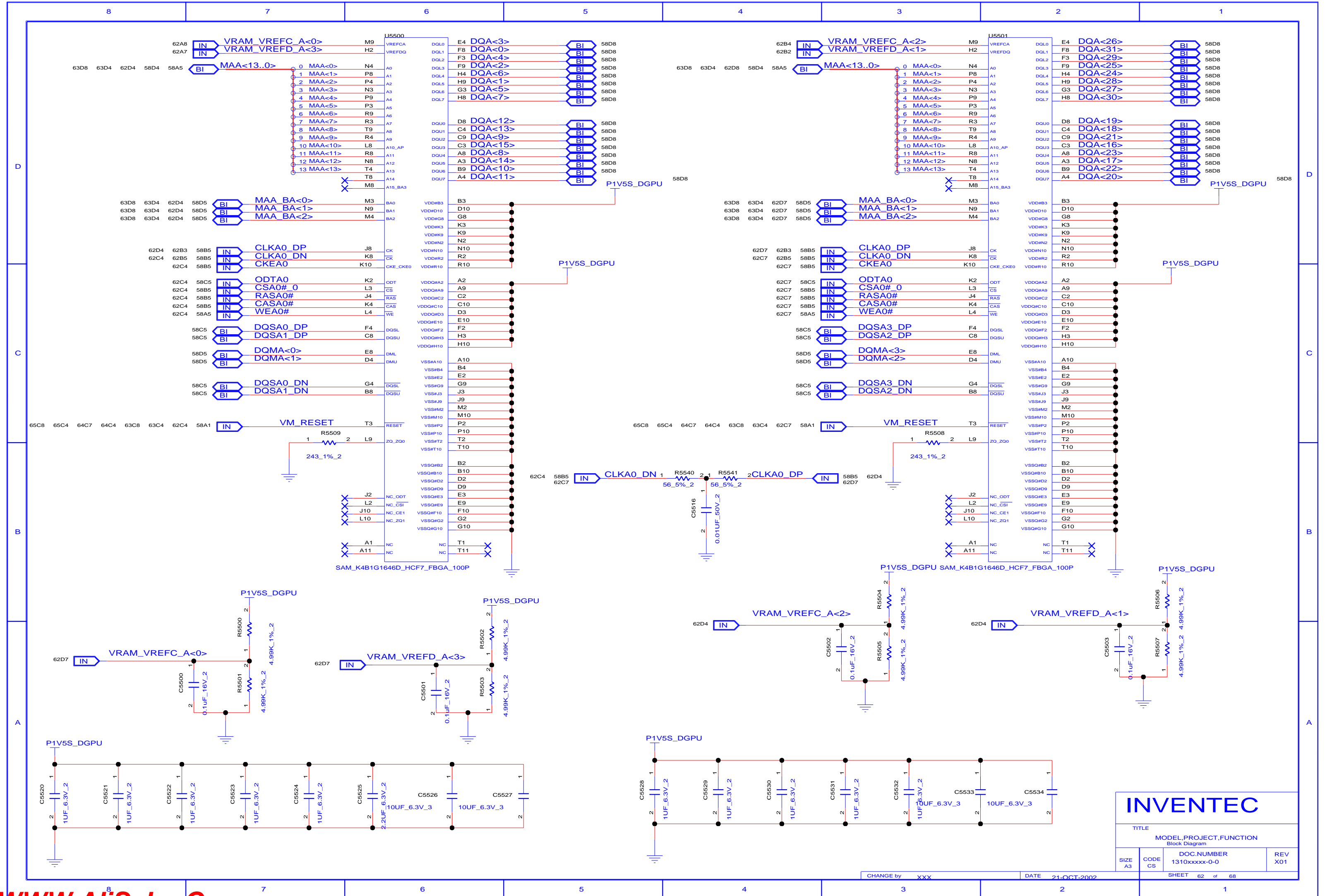
INVENTEC

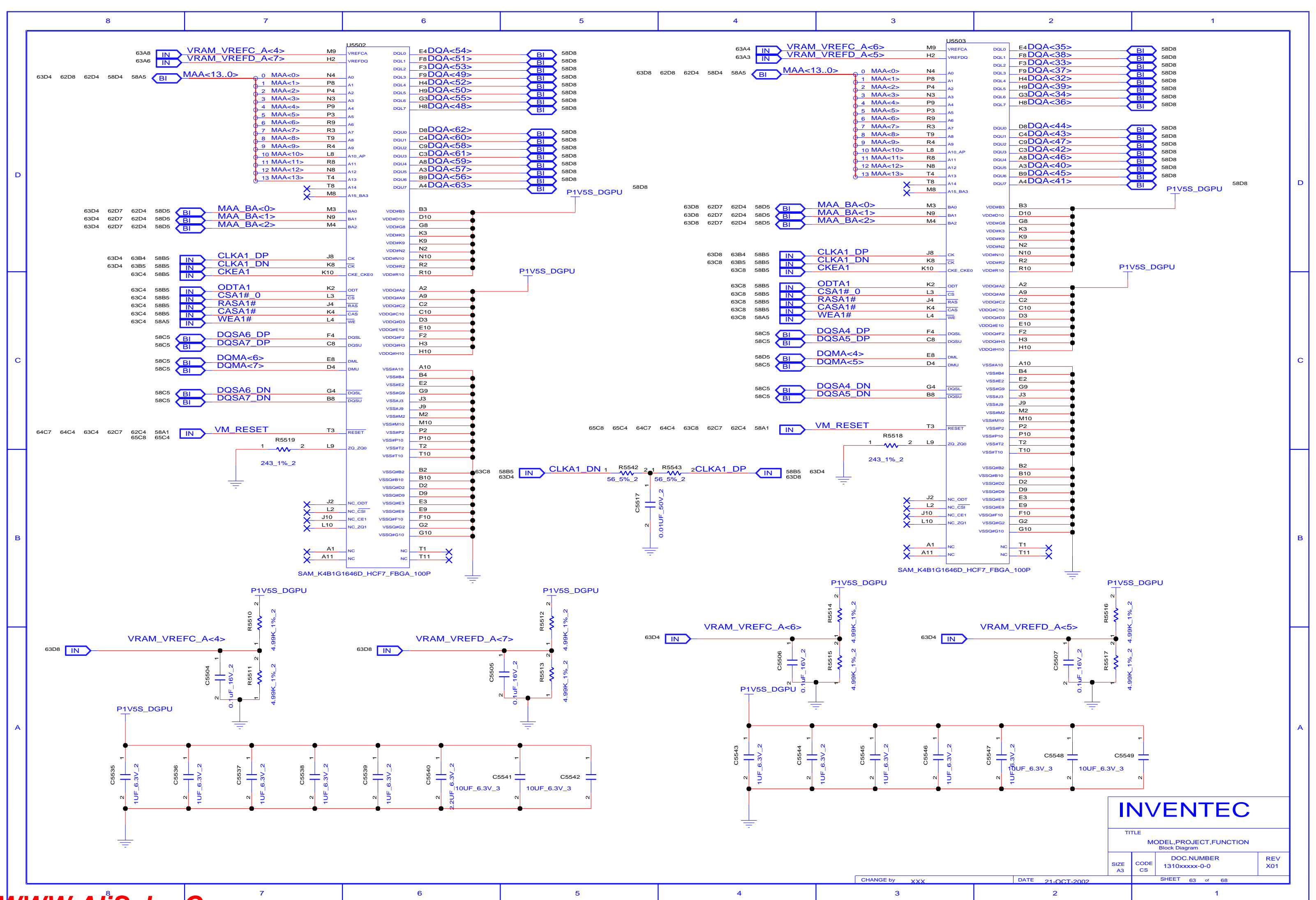
TITLE
MODEL, PROJECT, FUNCTION
Block Diagram

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

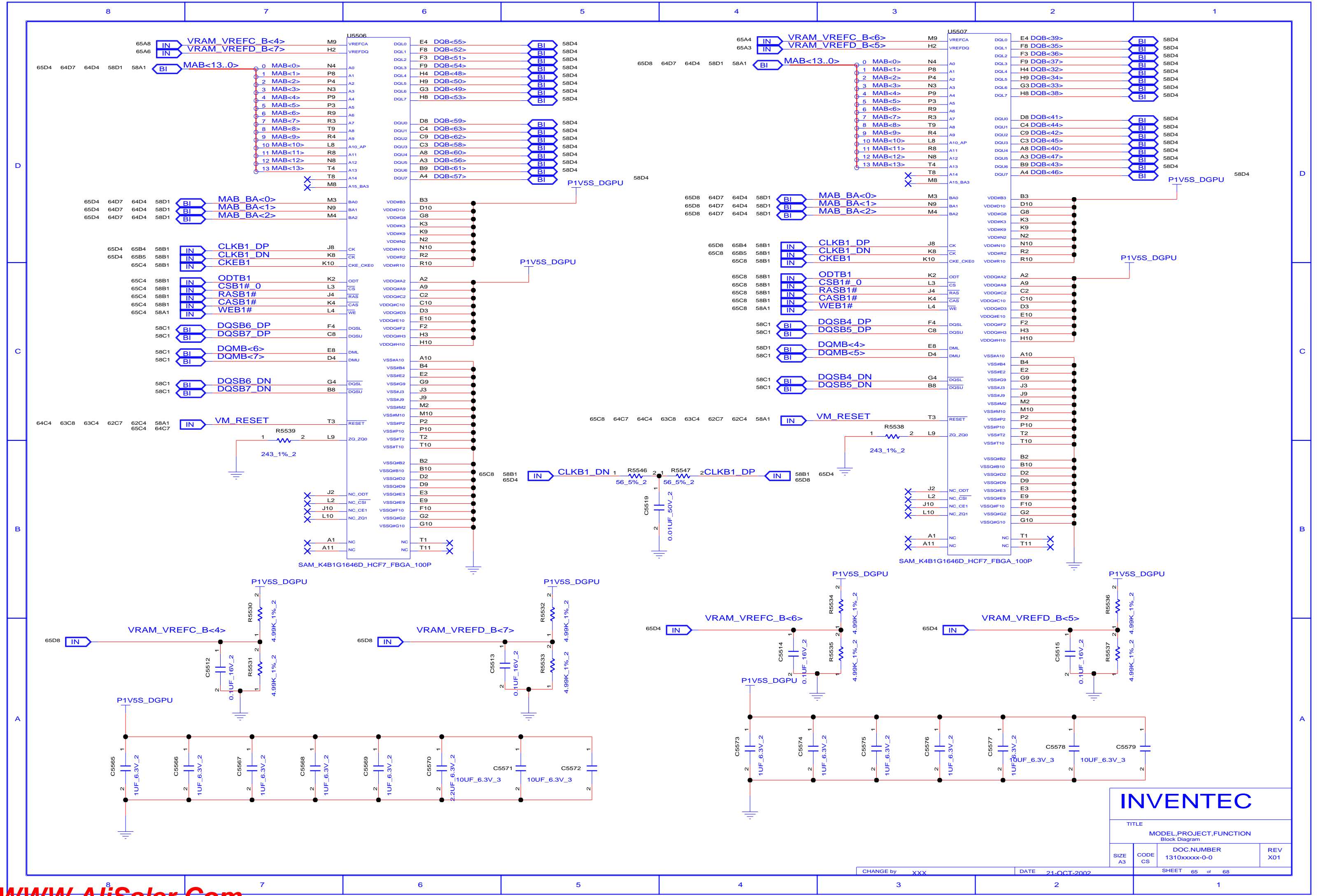
CHANGE by XXX DATE 21-OCT-2002 SHEET 60 of 68









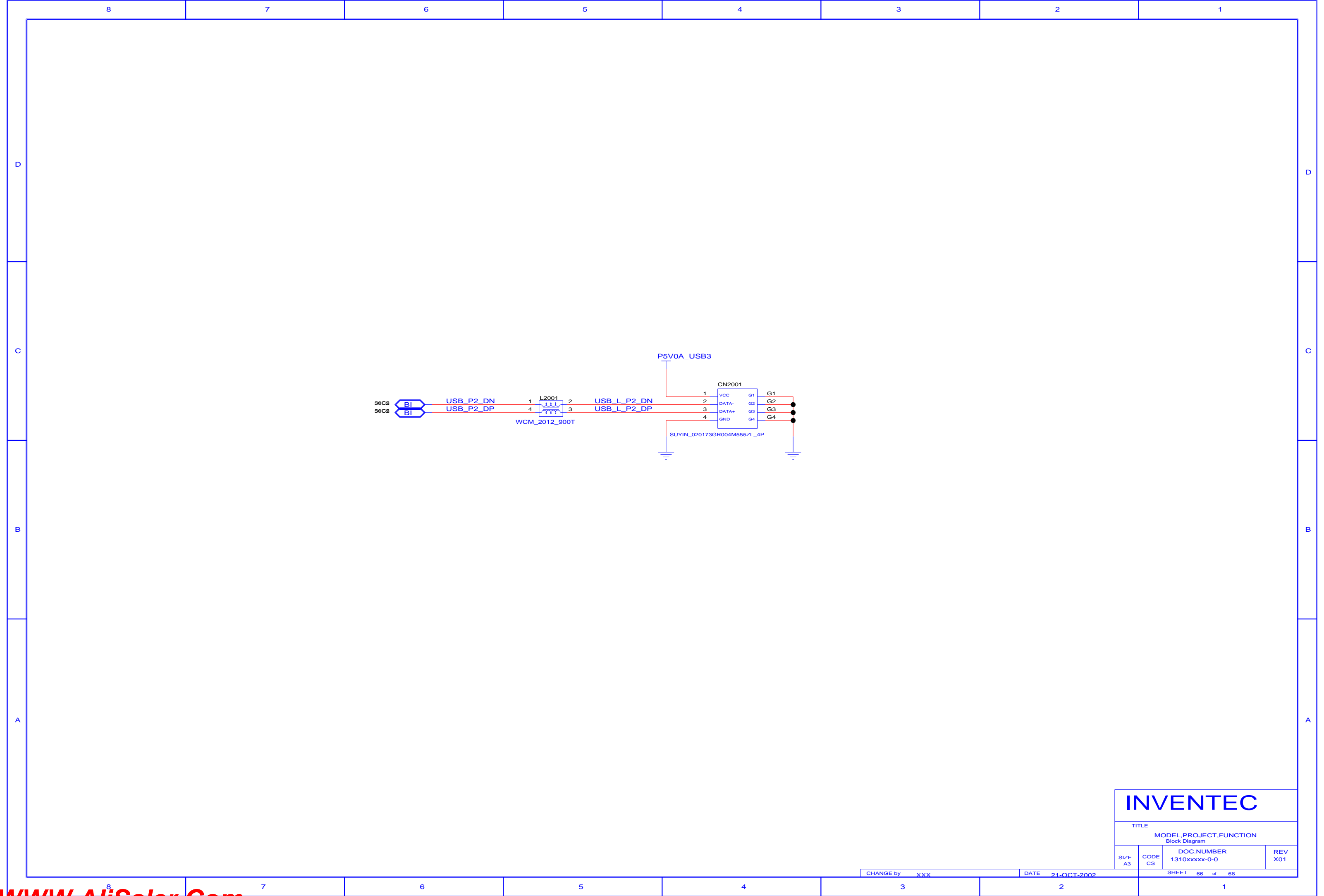


INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01

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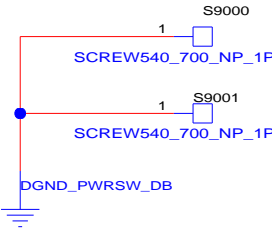
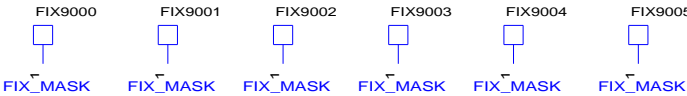
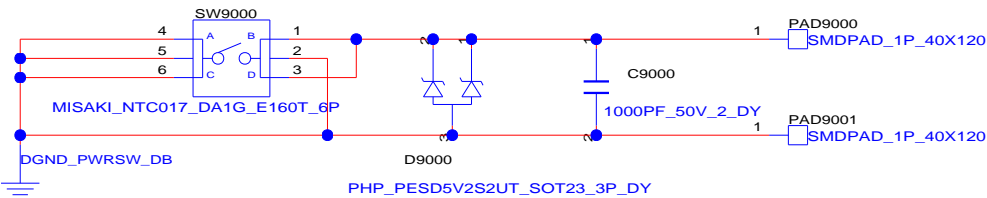
INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

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REFERENCE 9000~9999(SMALL BOARD)

POWER BUTTON



INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
CHANGE by XXX			
DATE 21-OCT-2002			
SHEET 67 of 68			

8		7		6		5		4		3		2		1	